#### COMPUTER ARCHITECTURE EXAM PRACTICE QUESTIONS

#### OUESTION 1:

 Simplify the following Boolean equation in terms of sum of product using four - variable Kmap and draw the logic circuit for the simplified equation.

$$F(A, B, C, D) = \sum_{i} (1,3,7,8,9,11,12,13,15)$$

(10 Marks)

- b) What is the purpose of using mapping process in memory management? Explain the two ( S Marks) different types of mapping process used in cache memory
- c) Perform the following operation:

(5 Marks)

- a) Add -4 and -2 using 2's complement and express the final answer in four bits.
- b) Simplify the following Boolean equation using basic identities of Boolean algebra.

Indicate the names of the laws.  $A = Y \cdot (W + X + (Y' + Z')') \cdot Z$ 

[Total = 20 marks]

#### OUESTION 2:

a) Design a half adder to add two binary bits: A and B.

(5 Marks)

- Explain S-R Flip-flop with logic circuit, graphic symbol and characteristics table. (5 Marks)
- Explain the working of read and write operations of magnetic disk with diagram. (5 Marks) d) Explain four-bit left shift register with diagram and example.

(5 Marks).

[Total = 20 marks]

#### OUESTION 3:

a) With block diagram explain the architecture of 8085 microprocessor.

(10 Marks)

With example explain three different types of instructions used in 8085 microprocessor

(5 Marks)

With example explain the different addressing modes used in 8085 microprocessor.

(5 Marks):

[Total = 20 marks]

# **Computer Architecture Test Questions**

**David Baud** 

#### **Computer Architecture Test Questions:**

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