

Analog to Digital Converter (ADC) Calibration Driver

[\[中文\]](#)

Introduction

In ESP32-S3, the digital-to-analog converter (ADC) compares the input analog voltage to the reference, and determines each bit of the output digital result. By design, the ADC reference voltage for ESP32-S3 is 1100 mV. However, the true reference voltage can range from 1000 mV to 1200 mV among different chips. This guide introduces the ADC calibration driver to minimize the effect of different reference voltages, and get more accurate output results.

Functional Overview

The following sections of this document cover the typical steps to install and use the ADC calibration driver:

- [Calibration Scheme Creation](#) - covers how to create a calibration scheme handle and delete the calibration scheme handle.
- [Result Conversion](#) - covers how to convert ADC raw result to calibrated result.
- [Thread Safety](#) - lists which APIs are guaranteed to be thread-safe by the driver.
- [Minimize Noise](#) - describes a general way to minimize the noise.

Calibration Scheme Creation

The ADC calibration driver provides ADC calibration scheme(s). From the calibration driver's point of view, an ADC calibration scheme is created for an ADC calibration handle `adc_cal_handle_t`.

`adc_cal_check_scheme()` can be used to know which calibration scheme is supported on the chip. If you already know the supported schemes, this step can be skipped. Just call the corresponding function to create the scheme handle.

If you use your custom ADC calibration schemes, you could either modify this function `adc_cal_check_scheme()`, or just skip this step and call your custom creation function.

ADC Calibration Curve Fitting Scheme

ESP32-S3 supports `ADC_CALI_SCHEME_VER_CURVE_FITTING` scheme. To create this scheme, set up `adc_cal_curve_fitting_config_t` first.

Converter Guide Programming

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Table of Contents Converter Guide Programming

1. Understanding the eBook Converter Guide Programming
 - The Rise of Digital Reading Converter Guide Programming
 - Advantages of eBooks Over Traditional Books
2. Identifying Converter Guide Programming
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Converter Guide Programming
 - User-Friendly Interface
4. Exploring eBook Recommendations from Converter Guide Programming
 - Personalized Recommendations
 - Converter Guide Programming User Reviews and Ratings
 - Converter Guide Programming and Bestseller Lists
5. Accessing Converter Guide Programming Free and Paid eBooks
 - Converter Guide Programming Public Domain eBooks
 - Converter Guide Programming eBook Subscription Services
 - Converter Guide Programming Budget-Friendly Options
6. Navigating Converter Guide Programming eBook Formats

- ePub, PDF, MOBI, and More
- Converter Guide Programming Compatibility with Devices
- Converter Guide Programming Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Converter Guide Programming
 - Highlighting and Note-Taking Converter Guide Programming
 - Interactive Elements Converter Guide Programming
- 8. Staying Engaged with Converter Guide Programming
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Converter Guide Programming
- 9. Balancing eBooks and Physical Books Converter Guide Programming
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Converter Guide Programming
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Converter Guide Programming
 - Setting Reading Goals Converter Guide Programming
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Converter Guide Programming
 - Fact-Checking eBook Content of Converter Guide Programming
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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