

Computer Architecture and Security

Fundamentals of Designing Secure Computer Systems

Shuangbao (Paul) Wang Robert S. Ledley







Computer Architecture And Security Fundamentals Of

Anupam Chattopadhyay

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Computer Architecture and Security Shuangbao Paul Wang, Robert S. Ledley, 2012-10-25 The first book to introduce computer architecture for security and provide the tools to implement secure computer systems This book provides the fundamentals of computer architecture for security It covers a wide range of computer hardware system software and data concepts from a security perspective It is essential for computer science and security professionals to understand both hardware and software security solutions to survive in the workplace Examination of memory CPU architecture and system implementation Discussion of computer buses and a dual port bus interface Examples cover a board spectrum of hardware and software systems Design and implementation of a patent pending secure computer system Includes the latest patent pending technologies in architecture security Placement of computers in a security fulfilled network environment Co authored by the inventor of the modern Computed Tomography CT scanner Provides website for lecture notes security tools and latest updates Computer Architecture and Organization Shuangbao Paul Wang, 2021-11-29 In today s workplace computer and cybersecurity professionals must understand both hardware and software to deploy effective security solutions This book introduces readers to the fundamentals of computer architecture and organization for security and provides them with both theoretical and practical solutions to design and implement secure computer systems Offering an in depth and innovative introduction to modern computer systems and patent pending technologies in computer security the text integrates design considerations with hands on lessons learned to help practitioners design computer systems that are immune from attacks Studying computer architecture and organization from a security perspective is a new area There are many books on computer architectures and many others on computer security However books introducing computer architecture and organization with security as the main focus are still rare This book addresses not only how to secure computer components CPU Memory I O and network but also how to secure data and the computer system as a whole It also incorporates experiences from the author's recent award winning teaching and research. The book also introduces the latest technologies such as trusted computing RISC V QEMU cache security virtualization cloud computing IoT and quantum computing as well as other advanced computing topics into the classroom in order to close the gap in workforce development The book is chiefly intended for undergraduate and graduate students in computer architecture and computer organization as well as engineers researchers cybersecurity professionals and middleware designers **Computer Architecture and Security** Shuangbao Paul Wang, Robert S. Ledley, 2013-01-10 The first book to introduce computer architecture for security and provide the tools to implement secure computer systems This book provides the fundamentals of computer architecture for security It covers a wide range of computer hardware system software and data concepts from a security perspective It is essential for computer science and security professionals to understand both hardware and software security solutions to survive in the workplace Examination of memory CPU architecture and system implementation Discussion of computer buses

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Architecture for Security provides practicing engineers and students with a clear understanding of computer hardware technologies. The fundamentals of digital logic design as well as the use of the Verilog hardware description language are discussed. The book covers computer organization and architecture modern design concepts and computer security through hardware. Techniques for designing both small and large combinational and sequential circuits are thoroughly explained. This detailed reference addresses memory technologies CPU design and techniques to increase performance microcomputer architecture including plug and play device interface and memory hierarchy. A chapter on security engineering methodology as it applies to computer architecture concludes the book Sample problems design examples and detailed diagrams are provided throughout this practical resource COVERAGE INCLUDES Combinational circuits small designs Combinational circuits large designs. Sequential circuits core modules Sequential circuits small designs Sequential circuits large designs. Memory Instruction set architecture Computer architecture interconnection Memory system Computer architecture security

Principles of Secure Processor Architecture Design Jakub Szefer, 2022-06-01 With growing interest in computer security and the protection of the code and data which execute on commodity computers the amount of hardware security features in today s processors has increased significantly over the recent years No longer of just academic interest security features inside processors have been embraced by industry as well with a number of commercial secure processor architectures available today This book aims to give readers insights into the principles behind the design of academic and commercial secure processor architectures Secure processor architecture research is concerned with exploring and designing hardware features inside computer processors features which can help protect confidentiality and integrity of the code and data executing on the processor Unlike traditional processor architecture research that focuses on performance efficiency and energy as the first order design objectives secure processor architecture design has security as the first order design objective while still keeping the others as important design aspects that need to be considered This book aims to present the different challenges of secure processor architecture design to graduate students interested in research on architecture and hardware security and computer architects working in industry interested in adding security features to their designs It aims to educate readers about how the different challenges have been solved in the past and what are the best practices i e the principles for design of new secure processor architectures Based on the careful review of past work by many computer architects and security researchers readers also will come to know the five basic principles needed for secure processor architecture design The book also presents existing research challenges and potential new research directions Finally this book presents numerous design suggestions as well as discusses pitfalls and fallacies that designers should avoid Network and Application Security Debashis Ganguly, 2011-11-11 To deal with security issues effectively knowledge of theories alone is not sufficient Practical experience is essential Helpful for beginners and industry practitioners this book develops a concrete outlook providing readers with basic concepts and an awareness of industry standards and best

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