A Power Reconfigurable High-Efficiency X-Band Power Amplifier MMIC Using the Load Modulated Balanced Amplifier Technique

J. R. Powell, D. J. Shepphard, R. Quaglia^o, Member, IEEE, and S. C. Cripps, Life Fellow, IEEE

Abstract—The load modulated balanced amplifier (LMBA) technique uses a control signal, injected at the output coupler, to modulate the impediance of the balanced amplifier transistors. A 14.1-W X-band LMBA is reported, integrating for the first time the balanced, driver, and control signal amplifiers in a single microwave monolithic integrated circuit. Load modulation and bias settings are used to demonstrate that high circuit efficiency can be achieved as the LMBA is adjusted for operation in three RF-power regimes; L.S. S.S., and 14.1 W for a constant input power of 22 dBm. Power-added efficiencies above 37% are observed in all power regimes from 8 to 9 GBz under saturated conditions.

Andex Terms — Balanced power amplifier, Doberty, gollium introde (GaN), load modulation, microwave monotithic integrated circuit (MABC).

1. INTRODUCTION

Power amplifier efficiency is a critical parameter for all applications. Maintaining efficiency as the outputpower requirement changes has been the subject of many inventions and papers. The Doberty technique [1], [2] has been widely adopted for its backed-off efficiency characteristic. Recently, a new technique, the load modulated balanced amplifier (LMBA) has been proposed and demonstrated [3]. In this technique, the output impedance seen by the transistors of a balanced amplifier is modulated using a control signal power (CSP) injected at the isolated port of the output.

Demonstrations of the LMBA technique have to date been confined to hybrid technologies and with a single-stage amplifier. The first published implementation [3] at 0.8–2 GHz yielded 60%–78% and 51%–67% drain efficiencies (DEs) at 42-dBm peak power and at 6-dB output-power back off (OPBO), respectively. A faither hybrid LMBA [4] operated from 4.5 to 7.5 GHz with 39-dBm peak power and 47%–77% DE. In [3], it was demonstrated that using two drain him states and three CSP power levels allows DE above 60%.

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J. R. Powell is with Skyama Ltd., Halescown B63 3TT, U.K. (n-mail: jeff.powell@skyama.com).

D. J. Shapphard, R. Quaglia, and S. C. Calpps are with the School of Engineering, Cardiff University, Cardiff CF10 3AC, U.K. (n-mail: quagliar tricardiff at air.)

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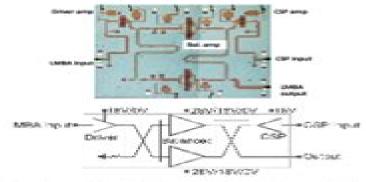


Fig. 1. Photograph of the LMRA MMTC fabricated using CaN 0.25 jumgate traflavidary at WEI Semiconductors (4 x 3.55 mm*), and block diagramreprocusing the recent integral functions in the LMRA.

to be maintained at >8 dB OPBO over regions of the band of operation. An extension of this letter [5] has employed a power splitter and on-board phase shifter network to allow a single input to be used in contrast to the separated CSP inputs in [3] and [4]. In this letter, an X-band LMBA in microwave monolithic integrated circuit (MMIC) technology is demonstrated for the first time. This NMIC was designed for specific applications which require discrete, rather than continuous, switched power levels with maximum efficiency.

III. DESCRIPTION

The LMBA MMIC incorporates a balanced amplifier pair, a driver amplifier, and a CSP amplifier, as shown in Fig. 1. The design objective was to demonstrate that load modulation could be employed to enable the amplifier to maintain efficiency at three discrete output power states; 14.1, 5.6, and 1.5 W by using various bias voltage states. The design strategy was to configure the balanced amplifier circuit for peak efficiency at a drain voltage of 18 V and use the LMBA function to reconfigure the balanced amplifier output mach when the balanced amplifier drain voltage is varied. In this way, the added power from the CSP will contribute to the overall higher power when the drain is increased to 28 V.

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