

Thus, the SPICE input file is as follows:

4.2 A layout of nMOS transistors in NAND2 gate is shown below. Write a SPICE description corresponding to the layout. The diffusion region between two polysilicon gates can be split equally between the two transistors.



4.3 Using the SPICE LEVEL 1 MOSFET model equations, derive an expression for the sensitivity of the drain current I_D with respect to temperature. Calculate the sensitivity at room temperature $T=300\text{K}$ by using the values in Example 4.1. For simplicity, assume that n_1 is independent of temperature. Also verify your solution numerically by calculating I_D at 310K and then $\Delta I_D/\Delta T$.

The sensitivity of drain current with respect to temperature T can be expressed by:

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