

Analog to Digital Converter (ADC) Calibration Driver

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Introduction

In ESP32-S3, the digital-to-analog converter (ADC) compares the input analog voltage to the reference, and determines each bit of the output digital result. By design, the ADC reference voltage for ESP32-S3 is 1100 mV. However, the true reference voltage can range from 1000 mV to 1200 mV among different chips. This guide introduces the ADC calibration driver to minimize the effect of different reference voltages, and get more accurate output results.

Functional Overview

The following sections of this document cover the typical steps to install and use the ADC calibration driver:

- [Calibration Scheme Creation](#) - covers how to create a calibration scheme handle and delete the calibration scheme handle.
- [Result Conversion](#) - covers how to convert ADC raw result to calibrated result.
- [Thread Safety](#) - lists which APIs are guaranteed to be thread-safe by the driver.
- [Minimize Noise](#) - describes a general way to minimize the noise.

Calibration Scheme Creation

The ADC calibration driver provides ADC calibration scheme(s). From the calibration driver's point of view, an ADC calibration scheme is created for an ADC calibration handle `adc_cal_handle_t`.

`adc_cal_check_scheme()` can be used to know which calibration scheme is supported on the chip. If you already know the supported schemes, this step can be skipped. Just call the corresponding function to create the scheme handle.

If you use your custom ADC calibration schemes, you could either modify this function `adc_cal_check_scheme()`, or just skip this step and call your custom creation function.

ADC Calibration Curve Fitting Scheme

ESP32-S3 supports `ADC_CALI_SCHEME_VER_CURVE_FITTING` scheme. To create this scheme, set up `adc_cal_curve_fitting_config_t` first.

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Bonnie Biafore



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