

David S. Hardin
Editor

Design and Verification of Microprocessor Systems for High-Assurance Applications

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Design And Verification Of Microprocessor Systems For High Assurance Applications

**Mike Hinchey,Jonathan P.
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Microprocessors increasingly control and monitor our most critical systems including automobiles airliners medical systems transportation grids and defense systems The relentless march of semiconductor process technology has given engineers exponentially increasing transistor budgets at constant recurring cost This has encouraged increased functional integration onto a single die as well as increased architectural sophistication of the functional units themselves Additionally design cycle times are decreasing thus putting increased schedule pressure on engineers Not surprisingly this environment has led to a number of uncaught design flaws Traditional simulation based design verification has not kept up with the scale or pace of modern microprocessor system design Formal verification methods offer the promise of improved bug finding capability as well as the ability to establish functional correctness of a detailed design relative to a high level specification However widespread use of formal methods has had to await breakthroughs in automated reasoning integration with engineering design languages and processes scalability and usability This book presents several breakthrough design and verification techniques that allow these powerful formal methods to be employed in the real world of high assurance microprocessor system design

Tools and Algorithms for the Construction and Analysis of Systems Bernd Finkbeiner, Laura

Kovács, 2024-04-03 The open access book 3 volume set LNCS 14570 14573 constitutes the proceedings of the 30th International Conference on Tools and Algorithms for the Construction and Analysis of Systems TACAS 2024 which was held as part of the European Joint Conferences on Theory and Practice of Software ETAPS 2024 during April 6 11 2024 in Luxembourg The 53 full papers and 16 short SVComp contributions included in these proceedings were carefully reviewed and selected from 159 submissions They were organized in topical sections as follows Part I STA and SMT solving synthesis logic and decidability program analysis and proofs proof checking Part II Model Checking automata and learning software verification probabilistic systems simulations Part III Neural networks testing and verification games concurrency SV Comp 2024

Certified Programs and Proofs Georges Gonthier, Michael Norrish, 2013-12-11 This book constitutes the refereed proceedings of the Third International Conference on Certified Programs and Proofs CPP 2013 colocated with APLAS 2013 held in Melbourne Australia in December 2013 The 18 revised regular papers presented together with 1 invited lecture were carefully reviewed and selected from 39 submissions The papers are organized in topical sections on code verification elegant proofs proof libraries certified transformations and security

Model Checking Software Owolabi Legunsen, Grigore Rosu, 2022-08-22 This book constitutes the refereed proceedings of the 28th International Symposium on Model Checking Software SPIN 2022 held virtually in May 2022 The 8 full papers were carefully reviewed and selected from 11 submissions Topics covered include formal verification techniques for automated analysis of software formal analysis for modeling languages such as UML state charts formal specification languages temporal logic design by contract model checking

automated theorem proving including SAT and SMT verifying compilers abstraction and symbolic execution techniques and much more

Interactive Theorem Proving Gerwin Klein, Ruben Gamboa, 2014-06-28 This book constitutes the proceedings of the 5th International Conference on Interactive Theorem Proving ITP 2014 Held as Part of the Vienna Summer of Logic VSL 2014 in Vienna Austria in July 2014 The 35 papers presented in this volume were carefully reviewed and selected from 59 submissions The topics range from theoretical foundations to implementation aspects and applications in program verification security and formalization of mathematics

Provably Correct Systems Mike Hinchey, Jonathan P. Bowen, Ernst-Rüdiger Olderog, 2017-03-01 As computers increasingly control the systems and services we depend upon within our daily lives like transport communications and the media ensuring these systems function correctly is of utmost importance This book consists of twelve chapters and one historical account that were presented at a workshop in London in 2015 marking the 25th anniversary of the European ESPRIT Basic Research project ProCoS Provably Correct Systems The ProCoS I and II projects pioneered and accelerated the automation of verification techniques resulting in a wide range of applications within many trades and sectors such as aerospace electronics communications and retail The following topics are covered An historical account of the ProCoS project Hybrid Systems Correctness of Concurrent Algorithms Interfaces and Linking Automatic Verification Run time Assertions Checking Formal and Semi Formal Methods Provably Correct Systems provides researchers designers and engineers with a complete overview of the ProCoS initiative past and present and explores current developments and perspectives within the field

Handbook of Model Checking Edmund M. Clarke, Thomas A. Henzinger, Helmut Veith, Roderick Bloem, 2018-05-18 Model checking is a computer assisted method for the analysis of dynamical systems that can be modeled by state transition systems Drawing from research traditions in mathematical logic programming languages hardware design and theoretical computer science model checking is now widely used for the verification of hardware and software in industry The editors and authors of this handbook are among the world's leading researchers in this domain and the 32 contributed chapters present a thorough view of the origin theory and application of model checking In particular the editors classify the advances in this domain and the chapters of the handbook in terms of two recurrent themes that have driven much of the research agenda the algorithmic challenge that is designing model checking algorithms that scale to real life problems and the modeling challenge that is extending the formalism beyond Kripke structures and temporal logic The book will be valuable for researchers and graduate students engaged with the development of formal methods and verification tools

Computer Safety, Reliability, and Security Andrea Bondavalli, Felicita Di Giandomenico, 2014-09-03 This book constitutes the refereed proceedings of the 33rd International Conference on Computer Safety Reliability and Security SAFECOMP 2014 held in Florence Italy in September 2014 The 20 revised full papers presented together with 3 practical experience reports were carefully reviewed and selected from 85 submissions The papers are organized in topical sections on fault injection techniques verification and validation techniques

automotive systems coverage models and mitigation techniques assurance cases and arguments system analysis security and trust notations languages for safety related aspects safety and security Computer Safety, Reliability, and Security Amund Skavhaug, Jérémie Guiochet, Friedemann Bitsch, 2016-09-06 This book constitutes the refereed proceedings of the 35th International Conference on Computer Safety Reliability and Security SAFECOMP 2016 held in Trondheim Norway in September 2016 The 24 revised full papers presented were carefully reviewed and selected from 71 submissions The papers are organized in topical sections on fault injection safety assurance formal verification automotive anomaly detection and resilience cyber security fault trees and safety analysis **Proceedings, Fifth IEEE International Symposium on High Assurance Systems Engineering (HASE 2000)** ,2000 The proceedings from this November 2000 conference in Albuquerque New Mexico feature 46 papers by computer scientists working with universities private companies and government agencies from around the world Topics include test based comprehension failure prediction models symposia benefits **Computer Assurance** ,1988 **Advanced Techniques for Microprocessor Systems** F. K. Hanna,1980 **COMPASS** ,1991 IBM Journal of Research and Development ,1982 Workshop on Industrial-Strength Formal Specification Techniques ,1995 Thirteen papers presented at the title workshop held in April 1995 in Boca Raton Fla whose objective was the analysis and synthesis of industrial applications of formal software development methods Topics include formal verification of the AAMP5 microprocessor timing analysis of industrial r NASA SP. ,1992 **Proceedings** ,1999 **Index to IEEE Publications** Institute of Electrical and Electronics Engineers,1990 Issues for 1973 cover the entire IEEE technical literature **Industrial Process Control Systems** L. Boullart, E. van Ravenzwaaij, J. P. Jansen, 1989 Hardbound With the much publicized industrial disasters which have occurred recently Chernobyl and Piper Alpha the importance of reliability and safety within industry has come to the forefront These Proceedings are divided into three sessions as follows industrial process control systems for safety applications expert systems and diagnostics and reliability procedures and guidelines Papers were presented on the basics of reliability and availability theory aiding techniques for example expert systems and software developments in a variety of areas ranging from mathematics to engineering These Proceedings will be a useful reference source for all those involved in the safety and maintenance of industrial systems Signal ,1991

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