



Fig 2.24 (a) NAND gate logic symbol.
(b) A Boolean expression for the output of a NAND gate

Circuit Diagram Of Nand Gate

M.V. Subramanyam



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Introduction to Digital Electronics and VHDL Mr. Sanjeev Pandey, 2024-08-16 Provides a foundation in digital electronics logic circuits and system design using VHDL emphasizing simulation synthesis and hardware implementation

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Flynn, 1973 **Digital Systems** Ronald J. Tocci, Neal S. Widmer, Gregory L. Moss, 2004 Tocci and Widmer use a block diagram approach to basic logic operations enabling readers to have a firm understanding of logic principles before they study the electrical characteristics of the logic ICs. KEY TOPICS For each new device or circuit the authors describe the principle of the operation give thorough examples and then show its actual application. An excellent reference on modern digital systems **British Miniature and Microminiature Electronic Assemblies Data Annual** , 1963

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