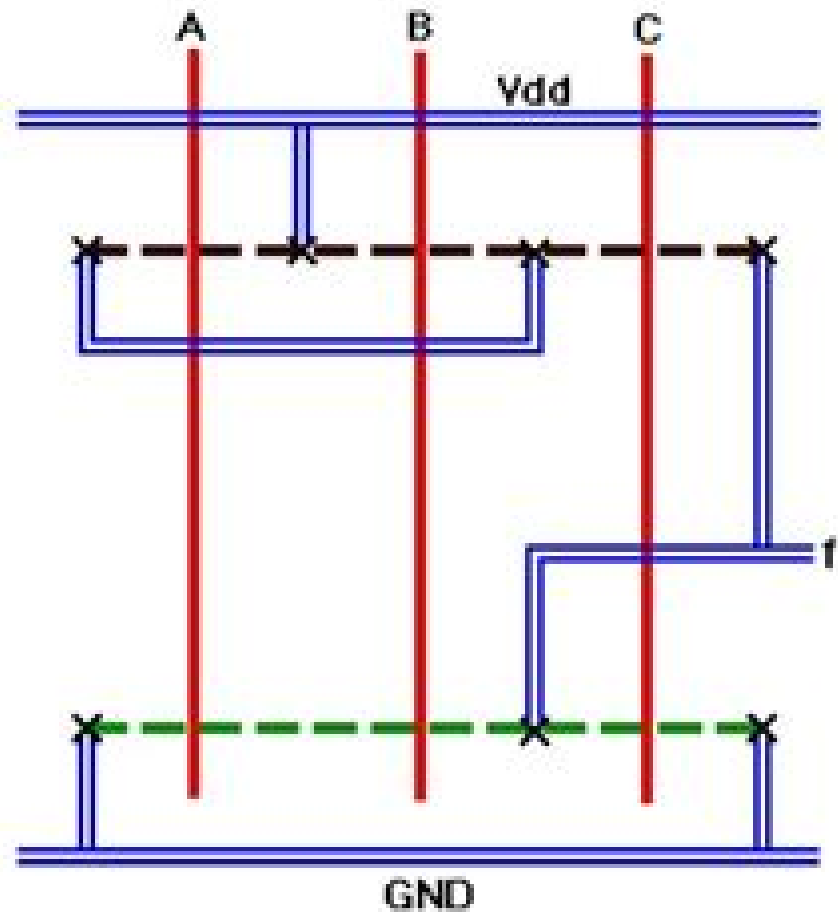
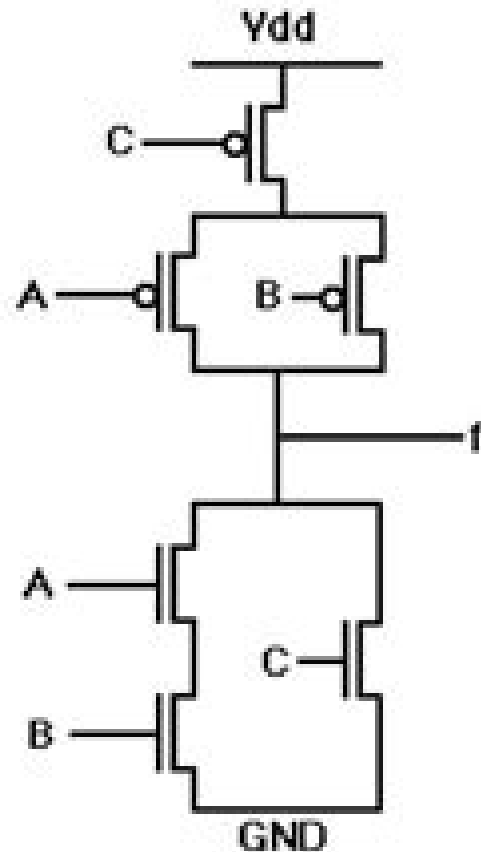


## Layout 2 (Different layout style to previous but same function being implemented)



Example:  $f = a \cdot b + c$

# Circuit Diagrams Translate Layout

**Peipei Pang**



## **Circuit Diagrams Translate Layout:**

**GB/T 25119-2010 English Translation of Chinese Standard** <https://www.codeofchina.com>, This Standard specifies the requirements of service design manufacture and testing of electronic equipment as well as basic hardware and software requirements considered necessary for durable and reliable equipment Additional requirements in other standards or specifications may complement this Standard if applicable List of subclauses of this Standard in which agreement between the parties is mentioned is detailed in Appendix B This Standard applies to all electronic equipment for control regulation protection supply etc installed on rail vehicles including subway and urban rail vehicle The equipment may be powered by the batteries or generators of vehicles or powered by a low voltage power supply with or without a direct connection to the contact system transformer voltage divider and auxiliary power supply For the purposes of this Standard electronic equipment is defined as equipment mainly composed of semiconductor devices and recognized associated components These components will mainly be mounted on printed boards Note sensors current voltage speed etc and firing unit printed board for power electronic equipment are covered by this Standard Complete firing units are covered by GB T 25122 1 This Standard is not applicable to the power electronic equipment in the main circuits and auxiliary circuits **VLSI Design** Norman G. Einspruch, 2014-12-01 VLSI Electronics Microstructure Science Volume 14 VLSI Design presents a comprehensive exposition and assessment of the developments and trends in VLSI Very Large Scale Integration electronics This volume covers topics that range from microscopic aspects of materials behavior and device performance to the comprehension of VLSI in systems applications Each article is prepared by a recognized authority The subjects discussed in this book include VLSI processor design methodology the RISC Reduced Instruction Set Computer the VLSI testing program silicon compilers for VLSI and specialized silicon compiler and programmable chip for language recognition Scientists engineers researchers device designers and systems architects will find the book very useful *Low Power VLSI Design* Angsuman Sarkar, Swapnadip De, Manash Chanda, Chandan Kumar Sarkar, 2016-08-08 This book teaches basic and advanced concepts new methodologies and recent developments in VLSI technology with a focus on low power design It provides insight on how to use Tanner Spice Cadence tools Xilinx tools VHDL programming and Synopsys to design simple and complex circuits using latest state of the art technologies Emphasis is placed on fundamental transistor circuit level design concepts Encyclopedia of Computer Science and Technology Allen Kent, James G. Williams, 1997-02-14 Artificial Intelligence in Economics and Management to Requirements Engineering **Symbolic Analysis for Automated Design of Analog Integrated Circuits** Georges Gielen, Willy M.C. Sansen, 2012-12-06 It is a great honor to provide a few words of introduction for Dr Georges Gielen s and Prof Willy Sansen s book Symbolic analysis for automated design of analog integrated circuits The symbolic analysis method presented in this book represents a significant step forward in the area of analog circuit design As demonstrated in this book symbolic analysis opens up new possibilities for the development of

computer aided design CAD tools that can analyze an analog circuit topology and automatically size the components for a given set of specifications Symbolic analysis even has the potential to improve the training of young analog circuit designers and to guide more experienced designers through second order phenomena such as distortion This book can also serve as an excellent reference for researchers in the analog circuit design area and creators of CAD tools as it provides a comprehensive overview and comparison of various approaches for analog circuit design automation and an extensive bibliography The world is essentially analog in nature hence most electronic systems involve both analog and digital circuitry As the number of transistors that can be integrated on a single integrated circuit IC substrate steadily increases over time an ever increasing number of systems will be implemented with one or a few very complex ICs because of their lower production costs

**Computer-Aided Design of Analog Integrated Circuits and Systems** Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06

The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today's new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck

Specifications and Drawings of Patents Relating to Electricity Issued by the U. S. , 1882 *Proceedings of the ... Midwest Symposium on Circuits and Systems* , 1995

**Design for Manufacturability** Artur Balasinski, 2013-10-05 This book explains integrated circuit design for manufacturability DfM at the product level packaging applications and applies engineering DfM principles to the latest standards of product development at 22 nm technology nodes It is a valuable guide for layout designers packaging engineers and quality engineers covering DfM development from 1D to 4D involving IC design flow setup best practices links to manufacturing and product definition for process technologies down to 22 nm node and product families including memories logic system on chip and system in package

**1989 IEEE International Symposium on Circuits and Systems** , 1989 *Electrical Engineering* , 1907 **Basic VLSI Design** Douglas A. Pucknell, Kamran Eshraghian, 1994

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