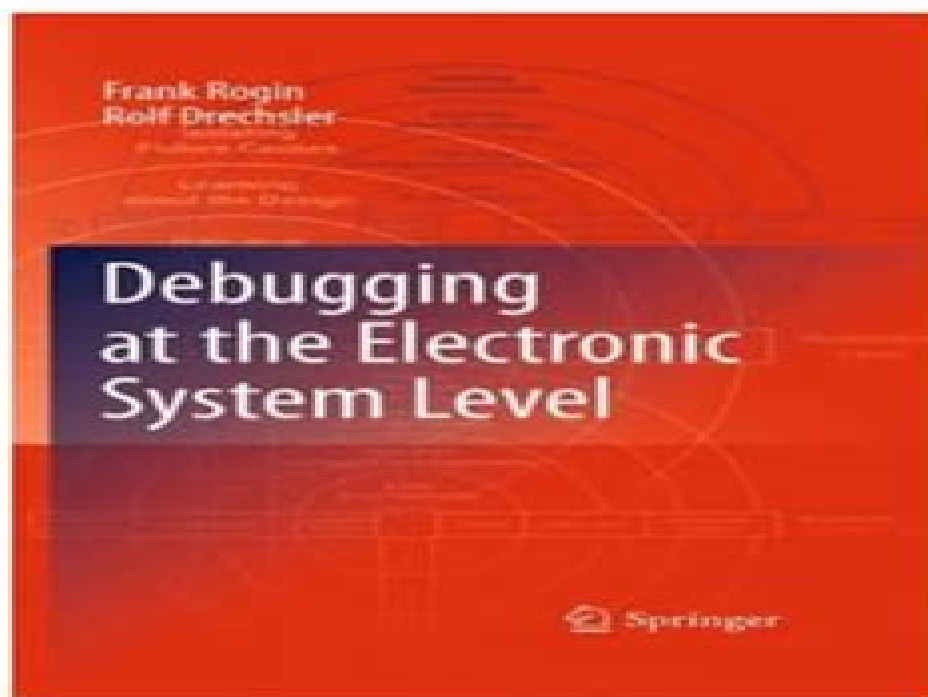


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# Debugging At The Electronic System Level

**Thomas Bernold, Günter  
Albers, Gottlieb Duttweiler-  
Institut, European Coordinating  
Committee for Artificial Intelligence**

## **Debugging At The Electronic System Level:**

**Debugging at the Electronic System Level** Frank Rogin, Rolf Drechsler, 2010-06-17 Debugging becomes more and more the bottleneck to chip design productivity especially while developing modern complex integrated circuits and systems at the Electronic System Level ESL Today debugging is still an unsystematic and lengthy process Here a simple reporting of a failure is not enough anymore Rather it becomes more and more important not only to find many errors early during development but also to provide efficient methods for their isolation In Debugging at the Electronic System Level the state of the art of modeling and verification of ESL designs is reviewed There a particular focus is taken onto SystemC Then a reasoning hierarchy is introduced The hierarchy combines well known debugging techniques with whole new techniques to improve the verification efficiency at ESL The proposed systematic debugging approach is supported amongst others by static code analysis debug patterns dynamic program slicing design visualization property generation and automatic failure isolation All techniques were empirically evaluated using real world industrial designs Summarized the introduced approach enables a systematic search for errors in ESL designs Here the debugging techniques improve and accelerate error detection observation and isolation as well as design understanding *Electronic Design* ,2005 [Electronic Engineering](#) ,2001

**Platform Based Design at the Electronic System Level** Mark Burton, Adam Morawiec, 2007-05-09 Platform Based Design at the Electronic System Level presents a multi faceted view of the set of problems that the electronic industry currently faces in the development and integration of complex heterogeneous systems including both hardware and software components It analyses and proposes solutions related to the provision of integration platforms by SoC and IP providers in light of the needs and requirements expressed by the system companies they are the users of such platforms which they apply to develop their next generation products Further the book tries to draw a comprehensive picture of the current interfaces between the platform providers and users defined by technical requirements current design methodology and flows standards and finally by the business context and relationships which should not to be underestimated These producer consumer shared interfaces enable or should enable the exchange of a well understood and complete set of data between both parties to ensure design efficiency high productivity and best use of domain specific expertise and knowledge The problems to be solved are related to modelling of platform functionality and performance formalisms methods metrics interoperability of models architecture exploration early SW development in parallel to the HW platform instantiation verification and debugging methods and flows management of complexity at various abstraction levels and the implications of the trade offs between the accuracy and complexity of models The solutions discussed by the contributors to this book have one common denominator these are standards In the general sense the book provides views on why and what kind of standards are the prerequisite to the deployment of a platform based design ecosystem in which cooperation is made possible between all parties involved in system development system houses platform and IP providers and EDA companies The

material presented in Platform Based Design at the Electronic System Level will help system architects system integrators design engineers IP developers and researchers to understand the state of the art and future tendencies in various aspects of modelling of system platforms     *Monitoring and Debugging of Distributed Real-time Systems* Jeffrey J.-P. Tsai, Steve J. H. Yang, 1995     *Microprocessors in Industry* Michael F. Horddeski, 1984     **Control System Interfaces** Michael F. Horddeski, 1992 Addresses process measurement and control illustrating how PCs have evolved in non control applications considering sophisticated process control software packages and discussing PS 2 and MetraBus boards and available drivers and so on     **Nanoelectronic Mixed-Signal System Design** Saraju Mohanty, 2015-02-20 Covering both the classical and emerging nanoelectronic technologies being used in mixed signal design this book addresses digital analog and memory components Winner of the Association of American Publishers 2016 PROSE Award in the Textbook Physical Sciences Mathematics category Nanoelectronic Mixed Signal System Design offers professionals and students a unified perspective on the science engineering and technology behind nanoelectronics system design Written by the director of the NanoSystem Design Laboratory at the University of North Texas this comprehensive guide provides a large scale picture of the design and manufacturing aspects of nanoelectronic based systems It features dual coverage of mixed signal circuit and system design rather than just digital or analog only Key topics such as process variations power dissipation and security aspects of electronic system design are discussed Top down analysis of all stages from design to manufacturing Coverage of current and developing nanoelectronic technologies not just nano CMOS Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability

*Design, Test and Certification Issues for Complex Integrated Circuits* L. Harrison, 1996     Electronic Components & Applications ,1989     JEE, Journal of Electronic Engineering ,1978     **EDN, Electrical Design News** ,2004

**Microcomputer Design** Michael F. Horddeski, 1986     Computer Aided Design of Digital Electronic Circuits and Systems Gerald Musgrave, 1979     **Electronic Products Magazine** ,1988     **Reports of Statistical Application Research** Nihon Kagaku Gijutsu Renmei, 1977     *Electronic Business Buyer* ,1987     **ESD** ,1988     Artificial Intelligence Thomas Bernold, Günter Albers, Gottlieb Duttweiler-Institut, European Coordinating Committee for Artificial Intelligence, 1985

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