

Digilent Nexys2 Board Reference Manual

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www.digilentinc.com

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Overview

The Nexys2 circuit board is a complete, ready-to-use circuit development platform based on a Xilinx Spartan 3E FPGA. Its on-board high-speed USB2 port, 16Mbytes of RAM and ROM, and several I/O devices and ports make it an ideal platform for digital systems of all kinds, including embedded processor systems based on Xilinx's MicroBlaze. The USB2 port provides board power and a programming interface, so the Nexys2 board can be used with a notebook computer to create a truly portable design station.

The Nexys2 brings leading technologies to a platform that anyone can use to gain digital design experience. It can host countless FPGA-based digital systems, and designs can easily grow beyond the board using any or all of the five expansion connectors. Four 12-pin Peripheral Module (Pmod) connectors can accommodate up to eight low-cost Pmods to add features like motor control, A/D and D/A conversion, audio circuits, and a host of sensor and actuator interfaces. All user-accessible signals on the Nexys2 board are ESD and short-circuit protected, ensuring a long operating life in any environment.

The Nexys2 board is fully compatible with all versions of the Xilinx ISE tools, including the free WebPack. Now anyone can build real digital systems for less than the price of a textbook.

Power Supplies

The Nexys2 board input power input bus can be driven from a USB cable, from a 5VDC-15VDC, center positive, 2.1mm wall-plug supply, or from a battery pack. A shorting block loaded on the "power select" jumper selects the power source. The USB circuitry is always powered from the USB cable – if no USB cable is attached, the USB circuitry is left unpowered.



- 500K-gate Xilinx Spartan 3E FPGA
- USB2-based FPGA configuration and high-speed data transfers (using the free Adept Suite Software)
- USB-powered (batteries and/or wall-plug can also be used)
- 16MB of Micron PSDRAM & 16MB of Intel StrataFlash ROM
- Xilinx Platform Flash for nonvolatile FPGA configurations
- Efficient switch-mode power supplies (good for battery powered applications)
- 50MHz oscillator plus socket for second oscillator
- 60 FPGA I/O's routed to expansion connectors (one high-speed Hirose FX2 connector and four 6-pin headers)
- 8 LEDs, 4-digit 7-seg display, 4 buttons, 8 slide switches
- Ships in a plastic carry case with USB cable

Figure 1: Nexys2 block diagram and features

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**National Engineering Laboratory (East
Kilbride)**



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FPGA [Xilinx] , **HCTL Open International Journal of Technology Innovations and Research (IJTIR)** Raj Gaurav Mishra,2013-07-31 HCTL Open International Journal of Technology Innovations and Research IJTIR ISSN Online 2321 1814 is an International Open Access Peer Reviewed Online journal devoted to various disciplines of Science and Technology HCTL Open IJTIR is a bi monthly journal published by HCTL Open Publications Solutions India and Hybrid Computing Technology Labs India Get more information at <http://ijtir.htcl.org> Diseño de sistemas empotrados de 8 bits en FPGAs con Xilinx ISE y Picoblaze Francisco Poza González,Luis Jacobo Álvarez Ruiz de Ojeda,2012-01-01 Este libro es dirigido a todos aquellos profesionales de la Electrónica tanto docentes como técnicos que deseen introducirse en el diseño de sistemas basados en microprocesadores de 8 bits empotrados embedded en FPGAs Field Programmable Gate Arrays En él se estudia el microprocesador de 8 bits Picoblaze de Xilinx para su implementación mediante FPGAs de las familias Spartan y Virtex de Xilinx Para poder trabajar con estos microprocesadores es necesario aprender el manejo de la herramienta ISE de Xilinx Este libro tiene su origen en la documentación elaborada para la impartición de varios cursos de posgrado sobre sistemas empotrados de 8 bits de Xilinx para el diseño de aplicaciones empotradas en FPGAs que se celebraron en el Departamento de Tecnología Electrónica de la Universidad de Vigo en los años 2005 a 2008 más información en http://www.dte.uvigo.es/logica_programable/cursos/htm Este libro tiene un nivel básico y para seguirlo el lector debe disponer de software gratuito de Xilinx y poseer los siguientes conocimientos previos Nivel básico del lenguaje VHDL Manejo de la herramienta ISE para el diseño con FPGAs Conocimientos de microprocesadores Nivel básico de lenguaje ensamblador Manejo básico del sistema operativo Windows En el libro se incluyen las presentaciones en Powerpoint utilizadas para explicar los siguientes apartados Teoría Introducción al diseño de Sistemas en un Circuito S O C Arquitectura de las FPGAs de la familia Spartan Microprocesador Picoblaze Arquitectura y juego de instrucciones Laboratorio Herramienta ISE Herramientas software del microprocesador Picoblaze Realización de circuitos de acoplamiento y periféricos para el microprocesador Picoblaze Diseño de sistemas digitales basados en el microprocesador Picoblaze Práctica Ejemplo básico con interrupción Reloj digital Sistema de gestión basado en RS232 Sistema de visualización con LCD **HiWire-Plus** Wintek,1990 **Kicad Pcbnew Reference Manual** Jean-Pierre Charras,2015-07-18 KiCad is an open source software suite for electronic design automation EDA It facilitates the design of schematics for electronic circuits and their conversion to PCBs printed circuit board design KiCad was originally developed by Jean Pierre Charras and features an integrated environment for schematic capture and PCB layout design This Reference Manual focuses on Pcbnew the printed circuit board editor There are other books in this series for tools like eeschema cvpcb gerbvew and more ISBC 569 Intelligent Digital Controller Board Hardware Reference Manual ,1979 CIEDS/circuit Board Design System (CBDS) Physical Layout Subsystem International Business Machines Corporation,1989 **XIL Reference Manual** Sun Microsystems,1994 **LINK Reference Manual** Digital Equipment

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web nov 10 2022 1 what have you achieved since your last review that made you feel proud employers use questions such as this to start the interview on a positive note the idea is to encourage you to reflect on your work and self assess your strengths it s also an opportunity to convey the value you re adding and the skills you offer how to answer