Chapter 3 SOLUTIONS

1 a) $I_D = \dfrac{V_{in} - 2V_{Don}}{R_1 + R_2}$

$= \dfrac{2.5 - 1.4}{4k\Omega} = \dfrac{1.1V}{4k\Omega}$

$\boxed{I_D = 275\mu A}$

b) $I_S = 10^{14}A$, $T = 300K$, $V_{Don} = 0.7V$

$I_D = I_S(e^{V_D/\phi_T} - 1)$

where $\phi_T = \dfrac{kT}{q} = 26mV$ @ 300 K

Ⓐ $I_D = \dfrac{V_{in} - 2V_D}{R_1 + R_2} = I_S(e^{V_D/\phi_T} - 1)$

$\dfrac{2.5 - 2V_D}{4k\Omega} = 10^{14}(e^{V_D/.026} - 1)$

iterating on this expression
we can obtain.

$\boxed{V_D = 0.628V}$
$\boxed{I_D = 311\mu A}$

c) SPICE

```
exercise 3.1

vin in 0 dc 2.5
r1 in 2 2k
d1 2 3 D
d2 3 out 2k
d2 out 0 D

.options post=2
.op

.model D D level=1 is=1e-14 sr=0.5

.end

*********************************
NODE      VOLTAGE
2         1.8750E+00
3         1.2500E+00
OUT       6.2501E-01

CURRENT   3.1269E-04
```

d) $I_S = 10^{-16}A$, $T = 300K$;
again use Ⓐ from b and iterate
to find:

$\boxed{V_D = 0.743V}$
$\boxed{I_D = 253\mu A}$

$I_S = 10^{14}A$, $T = 350K$;

$\boxed{V_D = 0.728V}$
$\boxed{I_D = 261\mu A}$

```
exercise 3.1d
vin in 0 dc 2.5
r1 in 2 2k
d1 2 3 D
r2 3 out 2k
d2 out 0 D

.options post=2
.op

*.temp27
.temp 77
*.model D D level=1 is=1e-16 sr=0.5
.model D D level=1 is=1e-14 sr=0.5
.end
*********************************
t=27C*
NODE      VOLTAGE
2         1.9889E+00
3         1.2500E+00
IN        2.5000E+00
OUT       7.3892E-01

ID        2.5554E-04
*********************************
t=77C*
NODE      VOLTAGE
2         1.7844E+00
3         1.2500E+00
IN        2.5000E+00
OUT       5.3436E-01

ID        3.5782E-04
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board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration   FPGA Prototyping by VHDL Examples Pong P. Chu,2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller   **Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation** Vassilis Paliouras,2005-09-06 This book constitutes the refereed proceedings of the 15th International Workshop on Power and Timing Optimization and Simulation PATMOS 2005 held in Leuven Belgium in September 2005 The 74 revised full papers presented were carefully reviewed and selected from numerous submissions The papers are organized in topical sections on low power processors code optimization for low power high level design telecommunications and signal processing low power circuits system on chip design busses and interconnections modeling design automation low power techniques memory and register files applications digital circuits and analog and physical design   **Integrated Circuit and System Design** ,2005   **Forthcoming Books** Rose Arny,2002-04   **The Proceedings of the European Design Automation Conference** ,1990   **Solutions Manual to Accompany Analysis and Design of Digital Integrated Circuits** David A. Hodges,Yu Chen,Horace G. Jackson,1983   *Solutions Manual Digital Integrated Circuits* CRC Press,2009-07-10 Any textbook more than five years old simply won t do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization Each chapter includes numerous worked examples case studies and SPICE computer simulations The book s website offers supplementary material and more worked problems Qualifying instructors will have access to a new instructor s manual   **Solutions Manual for Digital Integrated Circuits** Ayers John E,2003-09   Digital Integrated Circuits Jan M. Rabaey,2003 Contents p pChapter 1 Introductionp pChapter 2 The Manufacturing Processp pChapter 3 The Devicesp pChapter 4 The Wirep pChapter 5 The CMOS Inverterp pChapter 6 Designing Combinational Logic Gates in CMOS   **Digital Integrated Circuits** DeMassa,1996-02-01   **Digital Integrated Circuits** John E. Ayers,2018-09-03 Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years The second edition of Digital Integrated Circuits Analysis and Design focuses on timeless principles

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