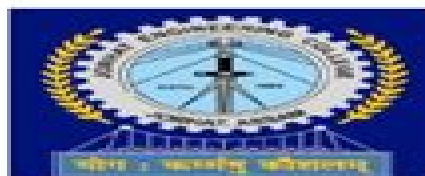
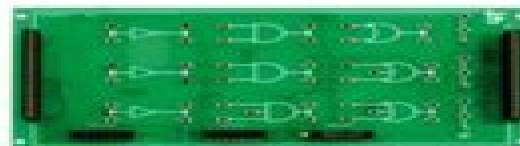


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## **Digital Systems and Logic Design**

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**Clemens Wendtner**



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