

# Digital Logic Circuit Analysis & Design **solution manual**



Second Edition



Victor P. Nelson  
Bill D. Carroll  
H. Troy Nagle  
J. David Irwin

# Digital Logic Circuit Analysis And Design

**Jun Yin, Yuan Zhang, Nazgul  
Shyngyssova, Chanjun Liu**



## **Digital Logic Circuit Analysis And Design:**

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The book provides basic concepts procedures and several relevant examples to help the readers to understand the analysis and design of various digital circuits It also introduces hardware description language VHDL The book teaches you the logic gates logic families Boolean algebra simplification of logic functions analysis and design of combinational circuits using SSI and MSI circuits and analysis and design of the sequential circuits This book provides in depth information about multiplexers de multiplexers decoders encoders circuits for arithmetic operations various types of flip flops counters and registers It also covers asynchronous sequential circuits memories and programmable logic devices      **Digital Circuit Analysis and Design** ,2005      □□□□□□□□□□ ,2023      *Electronic Circuit Analysis and Design* Donald A. Neamen,2001

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*Digital Circuit Design with an Introduction to CPLDs and FPGAs*  
Steven T. Karris, 2005 This book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits It supplements our Electronic Devices and Amplifier Circuits ISBN 0 9744239 4 7 It is self contained begins with the basics and ends with the latest developments of the digital technology The intent is to prepare the reader for advanced digital circuit design and programming the powerful Complex Programmable Logic Devices CPLDs and Field Programmable Gate Arrays FPGAs The prerequisites for this text are just basic high school math Accordingly it can be read and understood by high school seniors trade school community college and 4 year university students It is ideal for self study Chapter 1 is an introduction to the decimal binary octal and hexadecimal numbers their representation and conversion from one base to another Chapter 2 presents an introduction to arithmetic operations in binary octal and hexadecimal numbers The tens complement and nines complements in the decimal system and the twos complement and ones complements in the binary system are discussed and illustrated with numerous examples Chapter 3 begins with an introduction to sign magnitude representation of binary numbers It concludes with a discussion on floating point arithmetic for representing large numbers and the IEEE standard that specifies single precision 32 bit and double precision 64 bit floating point representation of numbers Chapter 4 describes the most commonly used binary codes The Binary Coded Decimal BCD the Excess 3 Code the 2 421 Code the Gray Code and the American Standard Code for Information Interchange ASCII code are introduced as well as the use of parity bits Chapter 5 begins with the basic logic operations and continues with the fundamentals of Boolean algebra and the basic postulates and theorems as applied to electronic logic circuits Truth

tables are defined and examples are given to illustrate how they can be used to prove Boolean algebra theorems or equivalent logical expressions Chapter 6 introduces the standard forms of expressing Boolean functions the minterms and maxterms also known as standard products and standard sums respectively A procedure is also presented to show how one can convert one form to the other This topic is essential in understanding the programming of Programmable Logic Arrays PLAs discussed in Chapter 11 Chapter 7 is an introduction to combinational logic circuits It begins with methods of implementing logic diagrams from Boolean expressions the derivation of Boolean expressions from logic diagrams input and output waveforms and the use of Karnaugh maps for simplifying Boolean expressions Chapter 8 is an introduction to sequential logic circuits It begins with a discussion of the different types of flip flops and continues with the analysis and design of binary counters registers ring counters and ring oscillators Chapter 9 is an introduction to computer memory devices We discuss the random access memory RAM read only memory ROM row and column decoders memory chip organization static RAMs SRAMs dynamic RAMs DRAMs volatile nonvolatile programmable ROMs PROMs Erasable PROMs EPROMs Electrically Erasable PROMs EEPROMs flash memories and cache memory Chapter 10 begins with an introduction to the basic components of a digital computer It continues with a discussion of the basic microprocessor operations and concludes with the description of more advanced arithmetic and logic operations We consider Chapter 11 as the highlight of this text It is an introduction to Field Programmable Devices FPDs also referred to as Programmable Logic Devices PLDs It begins with the description and applications of Programmable Logic Arrays PLAs continues with the description of Simple PLDs SPLDs and Complex PLDs CPLDs and concludes with the description of Field Programmable Gate Arrays FPGAs This text includes also four appendices Appendix A is an overview of the Advanced Boolean Equation Language ABEL which is an industry standard Hardware Description Language HDL used in Programmable Logic Devices PLDs Appendix B describes the VHSIC Hardware Description Language briefly referred to as VHDL This language was developed to be used for documentation verification and synthesis of large digital designs Appendix C introduces the Verilog Hardware Description Language HDL Like VHDL introduced in Appendix B Verilog is a programming language used to describe a digital system and its components Appendix D is a brief discussion on the boundary scan architecture and the new technology trends that make using boundary scan essential for the reduction in development and production costs

**Digital Electronics 2** Tertulien Ndjountche, 2016-08-16

As electronic devices become increasingly prevalent in everyday life digital circuits are becoming even more complex and smaller in size This book presents the basic principles of digital electronics in an accessible manner allowing the reader to grasp the principles of combinational and sequential logic and the underlying techniques for the analysis and design of digital circuits Providing a hands on approach this work introduces techniques and methods for establishing logic equations and designing and analyzing digital circuits Each chapter is supplemented with practical examples and well designed exercises with worked solutions This second of three volumes focuses on sequential and

arithmetic logic circuits It covers various aspects related to the following topics latch and flip flop binary counters shift registers arithmetic and logic circuits digital integrated circuit technology semiconductor memory programmable logic circuits Along with the two accompanying volumes this book is an indispensable tool for students at a bachelors or masters level seeking to improve their understanding of digital electronics and is detailed enough to serve as a reference for electronic automation and computer engineers Digital Integrated Circuits John E. Ayers, 2018-09-03 Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years The second edition of Digital Integrated Circuits Analysis and Design focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication this book delves into the dramatic advances in the field including new applications and changes in the physics of operation made possible by relentless miniaturization This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic Like the first edition this volume is a crucial link for integrated circuit engineers and those studying the field supplying the cross disciplinary connections they require for guidance in more advanced work For pedagogical reasons the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models With four new chapters more than 200 new illustrations numerous worked examples case studies and support provided on a dynamic website this text significantly expands concepts presented in the first edition

**Lab Manual** Gregory L. Moss, 1991 Digital Logic M. Rafiquzzaman, Steven A. McNinch, 2019-09-11 Digital Logic with an Introduction to Verilog and FPGA Based Design provides basic knowledge of field programmable gate array FPGA design and implementation using Verilog a hardware description language HDL commonly used in the design and verification of digital circuits Emphasizing fundamental principles this student friendly textbook is an ideal resource for introductory digital logic courses Chapters offer clear explanations of key concepts and step by step procedures that illustrate the real world application of FPGA based design Designed for beginning students familiar with DC circuits and the C programming language the text begins by describing of basic terminologies and essential concepts of digital integrated circuits using transistors Subsequent chapters cover device level and logic level design in detail including combinational and sequential circuits used in the design of microcontrollers and microprocessors Topics include Boolean algebra and functions analysis and design of sequential circuits using logic gates FPGA based implementation using CAD software tools and combinational logic design using various HDLs with focus on Verilog **Digital Logic Techniques** John Stonham, 2017-11-22 The third

edition of Digital Logic Techniques provides a clear and comprehensive treatment of the representation of data operations on data combinational logic design sequential logic computer architecture and practical digital circuits A wealth of exercises and worked examples in each chapter give students valuable experience in applying the concepts and techniques discussed Beginning with an objective comparison between analogue and digital representation of data the author presents the Boolean algebra framework for digital electronics develops combinational logic design from first principles and presents cellular logic as an alternative structure more relevant than canonical forms to VLSI implementation He then addresses sequential logic design and develops a strategy for designing finite state machines giving students a solid foundation for more advanced studies in automata theory The second half of the book focuses on the digital system as an entity Here the author examines the implementation of logic systems in programmable hardware outlines the specification of a system explores arithmetic processors and elucidates fault diagnosis The final chapter examines the electrical properties of logic components compares the different logic families and highlights the problems that can arise in constructing practical hardware systems

*DIGITAL LOGIC DESIGN* Sonali Singh, 2018-06-01 Description The book is an attempt to make Digital Logic Design easy and simple to understand The book covers various features of Logic Design using lots of examples and relevant diagrams The complete text is reviewed for its correctness This book is an outcome of sincere effort and hard work to bring concepts of Digital Logic Design close to the audience of this book The salient features of the book Easy explanation of Digital System and Binary Numbers with lots of solved examples Detailed covering of Boolean Algebra and Gate Level Minimization with proper examples and diagrammatic representation Detailed analysis of different Combinational Logic Circuits Complete Synchronous sequential Logic understanding Deep understanding of Memory and Programmable Logic Detailed analysis of different Asynchronous Sequential Logic Table Of Contents Unit 1 Digital System and Binary Numbers Part 1 Digital System and Binary Numbers Part 2 Boolean Algebra and Gate Level Minimization Unit 2 Combinational Logic Unit 3 Sequential Circuits Unit 4 Memory Programmable Logic and Design Unit 5 Asynchronous Sequential Logic



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