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Question Paper Code : 18159

M.E. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2016.

Elective

Applied Electronics

VL 7013 — VLSI FOR WIRELESS COMMUNICATION

(Common to M.E. Communication Systems/M.E. Communication and Networking
and M.E VLSI Design)

(Regulations 2013)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. How is noise Figure estimated using Frii's formula for a heterodyne receiver front end?
2. List out all the matching requirements of power amplifiers with the antenna for RF transmitter.
3. Define single side band Noise Figure for a mixer.
4. Write the conversion gain for the general V_h switching case for an unbalanced switching mixer.
5. How is the resolution of a divider circuit estimated when it has both a fixed-counter and programmable counter?
6. What is non-saturated delay cell in a ring oscillator?
7. What is meant by resolution of ADC?
8. What are the operating modes of adaptive equalizers?
9. What is multitier wireless architecture?
10. What is Direct Sequence Code Division Multiple Access?

Dsp Vlsi Question Papers

VM Jensen



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ICASSP 88: V & E, VLSI, spectral estimation ,1988 ICASSP 90: Digital signal processing ,1990 *ICASSP 88: D, digital signal processing* ,1988 2001 International Symposium on VLSI Technology, Systems, and Applications IEEE, Taipei Section Staff,2001 **Digest of Technical Papers** ,1987 *VLSI Logic Synthesis and Design* R. W. Dutton,1991

Very Good No Highlights or Markup all pages are intact **Digital Signal Processing** N. B. Jones,J. D. McK. Watson,1990

This volume presents the fundamentals of data signal processing ranging from data conversion to z transforms and spectral analysis In addition to presenting basic theory and describing the devices the material is complemented by real examples in specific case studies *Proceedings of Technical Papers* ,1997 **Scientific and Technical Aerospace Reports** ,1984-04

Bell Laboratories Talks and Papers Bell Telephone Laboratories. Libraries and Information Systems Center,1980

Proceedings, Real-Time Systems Symposium IEEE Computer Society,1985 *The Impact of Very High Performance Integrated Circuits on Radar, Guidance and Avionics Systems* North Atlantic Treaty Organization. Advisory Group for Aerospace Research and Development. Avionics Panel. Symposium,1985 **ICASSP 90: Multidimensional signal processing** ,1990 *ICASSP 90: Spectral estimation. Underwater signal processing* ,1990 **ICASSP 88: A & U, audio & electroacoustics, underwater signal processing** ,1988 **Proceedings of the Fourth IEEE Symposium on Parallel and Distributed Processing** ,1992 **ICASSP 90** ,1990 **ICASSP 87** ,1987 **Government Reports Announcements & Index** ,1996 High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures K. J. Ray Liu,Kung Yao,1998

Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today s rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures

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