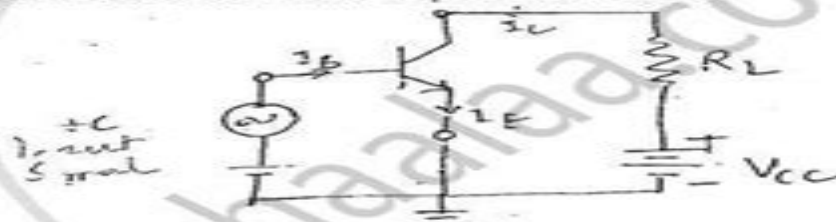


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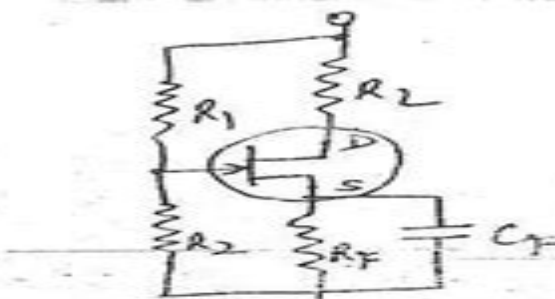
[Total Marks : 100

- N.B. (1) Question No. 1 is compulsory.
(2) Attempt any four from Question No. 2 to Question No. 7.
(3) Figures to the right indicates full marks.

1. (a) Why Zener diode is used as regulator in Power supply ? 20
(b) Differentiate between bridge rectifier and full wave center top rectifier.
(c) What are the advantages of negative feedback ?
(d) Differentiate JFET and BJT.
2. (a) Explain the construction, equivalent circuit V-I characteristics and working of Schottky diode. 10
(b) A half wave rectifier uses a diode with a forward resistance of 100Ω . If the input ac voltage is 220 V rms and the load resistance is 2 k Ω determine— 10
(i) I_{max} , I_{dc} and I_{rms} .
(ii) P/V when diode is ideal.
(iii) Load output voltage
(iv) dc output power.
3. (a) In CE configuration shown below, collector – supply voltage $V_{CC} = 10V$, load resistance R_L is 8 k Ω , draw the dc load line. Determine the operating point Q for zero signal if base current is 15 μ A and β is 40. 10



- (b) What is meaning of biasing a transistor ? Explain thermal runaway and hence importance of biasing with respect to a bijnction transistor. 10
4. (a) What is transconductance ? An N – channel JFET has a pinch off voltage of – 4.5 V and $I_{OSS} = 9$ mA. At what value of V_{GS} will I_{DS} equal to 3mA. What is its g_m at this I_{DS} . 10
(b) A JFET amplifier with stabilized biasing circuit as shown in Figure below has device par. meters $V_p = -2V$, $I_{OSS} = 5$ mA, $R_L = 910\Omega$, $R_F = 2.29$ k Ω , $R_1 = 12$ m Ω , $R_2 = 8.57$ m Ω and $V_{DD} = 24$ volt. Find the value of drain current I_D at the operating point. Also verify that FET will operate in pinch off region. 10



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