

Difference between CPLD & FPGA

Sr. No.	CPLD	FPGA
1.	Complex Programmable Logic Device	Field Programmable Gate Array
2.	It is collection PLDs.	It is collection of CLBs.
2.	Gate density up to 10000 gates	1000-few hundred 1000s gates
4.	Interconnection wise it is complex so called as CPLD.	Programmable at field or site so called as FPGA.
5.	PAL like blocks used as PLDs.	CLBs used as building blocks.
6.	AND-OR arrays are there in PAL like blocks.	LUTs are there in CLBs.
7.	Configuration context is stored in ROM.	Configuration context is stored in RAM.
8.	Configuration context is Non-volatile.	Configuration context is volatile.

Cpld And Fpga Architecture And Applications Previous Question Papers

Yi-Tong Ma



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In an electronic era where connections and knowledge reign supreme, the enchanting power of language has become more apparent than ever. Its ability to stir emotions, provoke thought, and instigate transformation is really remarkable. This extraordinary book, aptly titled "**Cpld And Fpga Architecture And Applications Previous Question Papers**," published by a highly acclaimed author, immerses readers in a captivating exploration of the significance of language and its profound impact on our existence. Throughout this critique, we will delve to the book's central themes, evaluate its unique writing style, and assess its overall influence on its readership.

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