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It is our pleasure to present the papers accepted for the 22nd International Workshop on Languages and Compilers for Parallel Computing held during October 8-10 2009 in Newark Delaware USA Since 1986 LCPC has become a valuable venue for researchers to report on work in the general area of parallel computing high performance computer architecture and compilers LCPC 2009 continued this tradition and in particular extended the area of interest to new parallel computing accelerators such as the IBM Cell Processor and Graphic Processing Unit GPU This year we received 52 submissions from 15 countries Each submission received at least three reviews and most had four The PC also sought additional external reviews for contentious papers The PC held an all day phone conference on August 24 to discuss the papers PC members who had a conflict of interest were asked to leave the call temporarily when the corresponding papers were discussed From the 52 submissions the PC selected 25 full papers and 5 short papers to be included in the workshop proceeding representing a 58% acceptance rate We were fortunate to have three keynote speeches a panel discussion and a tutorial in this year's workshop First Thomas Sterling Professor of Computer Science at Louisiana State University gave a keynote talk titled HPC in Phase Change Towards a New Parallel Execution Model Sterling argued that a new multi dimensional research thrust was required to realize the design goals with regard to power complexity clock rate and reliability in the new parallel computer systems ParalleX an exploratory execution model developed by Sterling's group was introduced to guide the co design of new architectures programming methods and system software

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