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CONTEMPORARY LOGIC DESIGN

SECOND EDITION

RANDY H. KATZ
GAETANO BORRIELLO

Contemporary Logic Design 2nd Edition Solution Manual

JS Bruner



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Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog. An SoPC system on a programmable chip integrates a processor, memory modules, I/O peripherals, and custom hardware accelerators into a single FPGA field programmable gate array device. In addition to the customized software, customized hardware can be developed and incorporated into the embedded system as well, allowing us to configure the soft core processor, create tailored I/O interfaces, and develop specialized hardware accelerators for computation intensive tasks. Utilizing an Altera FPGA prototyping board and its Nios II soft core processor, *Embedded SoPC Design with Nios II Processor and Verilog Examples* takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board. Emphasizing hardware design and integration throughout, the book is divided into four major parts. Part I covers HDL and synthesis of custom hardware. Part II introduces the Nios II processor and provides an overview of embedded software development. Part III demonstrates the design and development of hardware and software for several complex I/O peripherals, including a PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD secure digital card. Part IV provides several case studies of the integration of hardware accelerators, including a custom GCD greatest common divisor circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS direct digital frequency synthesis methodology. While designing and developing an embedded SoPC can be rewarding, the learning can be a long and winding journey. This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology.

Embedded SoPC Design with Nios II Processor and VHDL Examples Pong P.

Chu, 2011-08-29 The book is divided into four major parts. Part I covers HDL constructs and synthesis of basic digital circuits. Part II provides an overview of embedded software development with the emphasis on low level I/O access and drivers. Part III demonstrates the design and development of hardware and software for several complex I/O peripherals, including PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD secure digital card. Part IV provides three case studies of the integration of hardware accelerators, including a custom GCD greatest common divisor circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS direct digital frequency synthesis methodology. The book utilizes FPGA devices, Nios II soft core processor, and development platform from Altera Co, which is one of the two main FPGA manufacturers. Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions. Details at www.altera.com/university. The two main educational prototyping boards are known as DE1-99 and DE2-69. All experiments can be implemented and tested with these boards. A board combined with this book becomes a turn key solution for the SoPC design experiments and projects. Most HDL and C codes in the book are device independent.

and can be adapted by other prototyping boards as long as a board has similar I O configuration *FPGA Prototyping by VHDL Examples* Pong P. Chu, 2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller **Subject Guide to Books in Print**, 1996 **Catalog of Copyright Entries, Third Series** Library of Congress. Copyright Office, 1965 The record of each copyright registration listed in the Catalog includes a description of the work copyrighted and data relating to the copyright claim the name of the copyright claimant as given in the application for registration the copyright date the copyright registration number etc *Forthcoming Books* Rose Arny, 1999 **Paperbound Books in Print**, 1984 *Scientific and Technical Books and Serials in Print*, 1989 *British Books in Print*, 1970 **A catalogue of modern works on science and technology. 2nd, 4th, 5th, 7th, 8th, 10th-14th, 16th-19th, 22nd-25th, 35th, 39th, ed** sir George Christopher T. Bartley, 1875 **Bulletin** Nottingham (England). Public Libraries, 1910 The Nottingham Library Bulletin Nottingham Free Public Libraries, 1910 **Books in Print Supplement**, 2002 *The Publishers' Trade List Annual*, 1985 The British National Bibliography Arthur James Wells, 2000 **Engineering Education**, 1975 **A Catalogue of Modern Works on Science and Technology, Etc** Sir George Christopher Trout BARTLEY, 1874 Books and Pamphlets, Including Serials and Contributions to Periodicals Library of Congress. Copyright Office, 1976 **Catalogue of Title-entries of Books and Other Articles Entered in the Office of the Librarian of Congress, at Washington, Under the Copyright Law ... Wherein the Copyright Has Been Completed by the Deposit of Two Copies in the Office** Library of Congress. Copyright Office, 1977 *Journal of Mechanical Design*, 1979

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