BCA 171301

2024

B.C.A. 3rd Semester End-Term Examination

COMPUTER ARCHITECTURE AND ORGANIZATION

Full Marks - 70

1.

Time - Three hours

The figures in the margin indicate full marks for the questions.

Answer Question No. 1 and any four from the rest.

Answer the following (MCQ/ Fill in the blanks):					8
(ī)	RISC has				
	(a)	Many addressing modes			
	(b)	Less addressing modes			
	(c) Instructions are executed in multiple clock cycles				
	(d)	None of the above			
(iii)	Cache memory is				
	(a)	DRAM	(b)	SRAM	
	(c)	ROM	(d)	All of the above	
(111)	The transfers of data between memory and hard disk is done using				
	(a)	Interrupt-initiated I/O	(b)	DMA	
	(c)	Programmed I/O	(d)	Synchronous transmission	
(iv)	Mic	rocontroller can have			
	(n)	ROM	- (b)	RAM	
	(c)	CPU	(d)	All of the above	
		FON REDMI 7 JAL CAMERA		[Turn over	

<u>Computer Organization And Architecture Bca Question</u> <u>Paper</u>

Chopra Rajiv

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