



Dell PowerEdge R710—Information Update

System Memory

This document provides latest information on the supported memory configurations listed in the *Hardware Owner's Manual* at support.dell.com/manuals.

For systems with two processors:

- Single-rank and dual-rank RDIMMs of sizes 2 GB, 4 GB, 8 GB, and 16 GB are supported for a total of up to 288 GB.
- Quad-rank RDIMMs (two per channel) are supported for a total of up to 192 GB.
- 1 GB and 2 GB UDIMMs are supported for a total of up to 24 GB.

Memory Module Installation Guidelines

The memory speed of each channel depends on the memory configuration:

- For single or dual-rank memory modules:
 - One and two memory modules per channel support up to 1333 MHz.
 - Three memory modules per channel support up to 800 MHz.
- For quad-rank memory modules:
 - One memory module per channel supports up to 1067 MHz.
 - Two memory modules per channel are limited to 800 MHz, regardless of memory module speed.



NOTE: Actual memory speed depends on your system configuration such as the processor series, DIMM voltage (low or standard), and system firmware/BIOS version.

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Computer Architecture John L. Hennessy,David A. Patterson,2017-11-23 Computer Architecture A Quantitative Approach Sixth Edition has been considered essential reading by instructors students and practitioners of computer design for over 20 years The sixth edition of this classic textbook from Hennessy and Patterson winners of the 2017 ACM A M Turing Award recognizing contributions of lasting and major technical importance to the computing field is fully revised with the latest developments in processor and system architecture The text now features examples from the RISC V RISC Five instruction set architecture a modern RISC instruction set developed and designed to be a free and openly adoptable standard It also includes a new chapter on domain specific architectures and an updated chapter on warehouse scale computing that features the first public information on Google s newest WSC True to its original mission of demystifying computer architecture this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening while always keeping an emphasis on good engineering design Winner of a 2019 Textbook Excellence Award Texty from the Textbook and Academic Authors Association Includes a new chapter on domain specific architectures explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore s Law and Dennard scaling Features the first publication of several DSAs from industry Features extensive updates to the chapter on warehouse scale computing with the first public information on the newest Google WSC Offers updates to other chapters including new material dealing with the use of stacked DRAM data on the performance of new NVIDIA Pascal GPU vs new AVX 512 Intel Skylake CPU and extensive additions to content covering multicore architecture and organization Includes Putting It All Together sections near the end of every chapter providing real world technology examples that demonstrate the principles covered in each chapter Includes review appendices in the printed text and additional reference appendices available online Includes updated and improved case studies and exercises ACM named John L Hennessy and David A Patterson recipients of the 2017 ACM A M Turing Award for pioneering a systematic quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry **Middleware 2012** Priya Narasimhan,Peter Triantafillou,2012-11-11 This book constitutes the refereed proceedings of the ACM IFIP USENIX 13th International Middleware Conference held in Montreal Canada in December 2012 The 24 revised full papers presented were carefully

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John Yani Arrasjid, VCDX-001, Mark Gabryjelski, VCDX-023, Chris McCain, VCDX-079, 2012-03-20 The first book in the IT Architect series helps aspiring experienced IT infrastructure architects administrators and those pursuing infrastructure design certifications establish a solid foundation in the art of infrastructure design The three autho **CIO.**, 2009 [Info](#)
[exame](#), 2009 □□□□□□, 2013-04 **Dell Inspiron 9100 Owner's Manual**, 2004 **Dell USB 3.0 Ultra HD 4K Triple Display Docking Station User's Manual** Dell Aspire D3rou, 2019-05-14 Unofficial User Guide The Dell Docking Station USB 3 0 connects your laptop to up to three additional monitors various external devices and the Internet with a single cable It features a convenient way to expand the capabilities of your portable PC and customize your desktop computing environment This sleek compact dock unlocks enhanced productivity and entertainment options with highspeed connectivity and support for Ultra HD 4K displays It also includes three USB 3 0 ports as well as two USB 2 0 connections ensuring compatibility with a vast array of peripherals such as external hard drives printers and scanners Moreover it is a great choice for corporate environments with Wake On LAN and PXE boot on selected platforms **Dell USB 3.0 Ultra HD 4K Triple Display Docking Station User's Manual** Dell Aspire Bhr7o, 2019-05-23 Unofficial User Guide The Dell Docking Station USB 3 0 connects your laptop to up to three additional monitors various external devices and the Internet with a single cable It features a convenient way to expand the capabilities of your portable PC and customize your desktop computing environment This sleek compact dock unlocks enhanced productivity and entertainment options with highspeed connectivity and support for Ultra HD 4K displays It also includes three USB 3 0 ports as well as two USB 2 0 connections ensuring compatibility with a vast array of peripherals such as external hard drives printers and scanners Moreover it is a great choice for corporate environments with Wake On LAN and PXE boot on selected platforms

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