LEACH ENGINEERING : Electrical Engineering (Sandwick Pattern) Programme : District Circuit & Network Course Table Indiana Sin FE 23 E E 0 0 2 2:30 Hours / 60 marks Andrew Science Attend of the parations. Manufacture some ancests with next that they wherever necessary Use of Mathematical Tables, Steam Table and Product Colonians (non-programmable) is permissible Marks on Right Fland Sale indicate full marks for the question. Assessed mobalitie additional data, if accountry CONCURRENCE BOOMER, T-SEPTEN 12 Marks Attempt are NIX 0.1 a. Drow the phases diagram to represent the following softspan. [CO4LL-U] $V_1 = 50 \text{ min set}, V_2 = 200 \text{ min set} = 10$ ICO-1, 1-401 Define : in Admittance ii) Conductance ICO-3, L-RI What is phase sequence? What is phase sequence Define it Mosh its North 1CO-4, L-80 (CO-4, L-R) State Raytonocity Thornton 100-L-L-RI Draw the phases diagram for R-L series circuit. (CO-LL-R) State Norton's Theorem. For the given impoduces triangle as shown in the fig I Month the type of circuit. (CO-2, L-U) Make parameters of all sides of the binegle. (i) Define- is Koacine Power iii Apparent Power [CO-L LAR] Attempt my THEEL. 0.3 12 Marks a. Derive the expression for current in pure capacitive current when connected in sinusoidd AC voltage. Draw the phases diagram. DCO-4, L-61 Note any four advantages of polyphore circuit over single phase circuit. 300-A, L-RI-Compare between series A parallel resonance circuit on the basic of the following points is Resonance frequency as Impolance to Current in Magnification 1004, 641 Using Migh Analysis find surrest flowing through 4 (2 resistance in fig. 2) DOM: LAND 0.3 Attempt any THREE. 12 Marks Explain Coupling Representation of Resonance for series reconstructed is. Impediance $Z_1 = (30 \circ 25) \times 3$ and $Z_2 = (8 \circ 26) \times 3$ are connected in parallel account V vA (201 v)00 volts. Using the admittance method, colorate the circum statement and beats, he successes. s. Near relationship between low unlarge and place voltage, line content and phase current in a behavior delta commercion. Days acceptable phases disasters of

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D. Marka

Calculate current through It Cl resistance using Nortee's thereton as thereto. 3000-4, L-All

Amount on FOUR With most circuit diagram copiate AC through prex memorics about. With expension of voltage, current & power. ROOT, LAD

Using would veltage melted, find eigenst in M) assistance for network therein ROB-4, 1-All

A balanced delta-connected head basing per plane impedance of (3+y6) (3 in

commuted to 400 Y, 7 & a. c. supply. Determine is I are commit its Private Section, 502 Active person 200-3-1-41

State the Phenomin's Thomas and also drow the Theoretic's naturals. 1004.1411

a. A cold having resistance of 5 G and an industance of 0.7 H is commerced to parallel with a woder combination of 10 O mainer and 80 off appealine. M. supply induge is 250 V, 50 Hz.

Determine () Total about impolance, ii) Total current taken by the circuit. 900-2, L-A1 in) Person Sigter of the circuit.

Attempt on TWO In the following natural, as shown in Fig. 5, find value of resistance Fig. 80

12 Marks

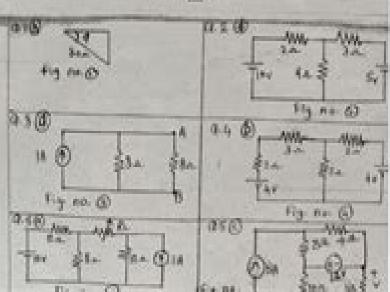
that maximum power will reasolar through it and also calculate this power.

 A series B.L.C street has B= 5 Ω, L = 10 mH and C = 15 at Colorida. i) Property of which the costal between an a purely inside to clock 10:10 fluster of the obsolit. DOMESTICAL PROPERTY. His Voltage teach Seption.

 Determine the voltage 'V' moves I th resiste in network there in fig I unleg SCOLAR LAND

Superposition Theorem.

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