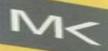
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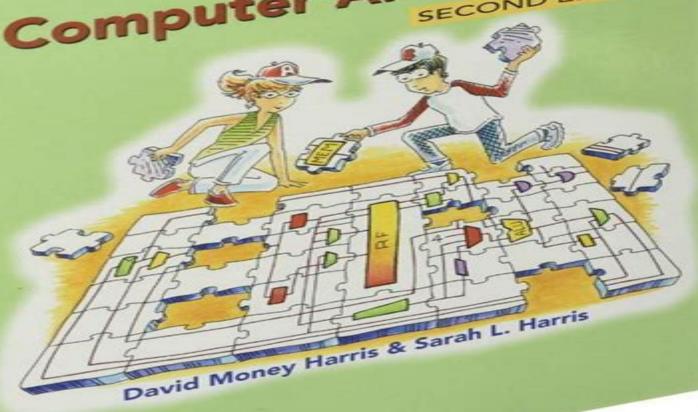
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EDITION





Digital Design and Computer Architecture



Digital Design Computer Architecture 2nd Edition

John Patrick Hayes

Digital Design Computer Architecture 2nd Edition:

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based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun's RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun's RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems Digital Design and Computer Architecture David Harris, Sarah Harris, 2015-04-22 Digital Design and Computer Architecture ARM Edition takes a unique and modern approach to digital design Beginning with digital logic gates and progressing to the design of combinational and sequential circuits Harris and Harris use these fundamental building blocks as the basis for what follows the design of an actual ARM processor With over 75% of the world's population using products with ARM processors the design of the ARM processor offers an exciting and timely application of digital design while also teaching the fundamentals of computer architecture SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design By the end of this book readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works Harris and Harris have combined an engaging and humorous writing style with an updated and hands on approach to digital design Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Features side by side examples of the two most prominent Hardware Description Languages HDLs SystemVerilog and VHDL which illustrate and compare the ways each can be used in the design of digital systems Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises Digital Design and Computer Architecture David Money Harris, Sarah L. Harris, 2015 Digital Design and Computer Architecture David Maney Harris, Sarah L. Harris, 2007 **Digital Design And Computer Architecture David**

Money Harris, 2009-01-01 **Principles of Verilog Digital Design** Wen-Long Chin, 2022-02-27 Covering both the fundamentals and the in depth topics related to Verilog digital design both students and experts can benefit from reading this book by gaining a comprehensive understanding of how modern electronic products are designed and implemented Principles of Verilog Digital Design contains many hands on examples accompanied by RTL codes that together can bring a beginner into the digital design realm without needing too much background in the subject area This book has a particular focus on how to transform design concepts into physical implementations using architecture and timing diagrams Common mistakes a beginner or even an experienced engineer can make are summarized and addressed as well Beyond the legal details of Verilog codes the book additionally presents what uses Verilog codes have through some pertinent design principles Moreover students reading this book will gain knowledge about system level design concepts Several ASIC designs are illustrated in detail as well In addition to design principles and skills modern design methodology and how it is carried out in practice today are explored in depth as well Digital Logic M. Rafiguzzaman, Steven A. McNinch, 2019-09-11 Digital Logic with an Introduction to Verilog and FPGA Based Design provides basic knowledge of field programmable gate array FPGA design and implementation using Verilog a hardware description language HDL commonly used in the design and verification of digital circuits Emphasizing fundamental principles this student friendly textbook is an ideal resource for introductory digital logic courses Chapters offer clear explanations of key concepts and step by step procedures that illustrate the real world application of FPGA based design Designed for beginning students familiar with DC circuits and the C programming language the text begins by describing of basic terminologies and essential concepts of digital integrated circuits using transistors Subsequent chapters cover device level and logic level design in detail including combinational and sequential circuits used in the design of microcontrollers and microprocessors Topics include Boolean algebra and functions analysis and design of sequential circuits using logic gates FPGA based implementation using CAD software tools and combinational logic design using various HDLs with focus on Verilog Digital Design (VHDL) Peter J. Ashenden, 2007-10-24 Digital Design An Embedded Systems Approach Using VHDL provides a foundation in digital design for students in computer engineering electrical engineering and computer science courses It takes an up to date and modern approach of presenting digital logic design as an activity in a larger systems design context Rather than focus on aspects of digital design that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills Hardware description language HDL based design and verification is emphasized VHDL examples are used extensively throughout By treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components Includes a Web site with links to vendor tools labs and tutorials Presents digital logic design as an activity in a larger systems design context Features extensive use of VHDL examples to demonstrate HDL hardware description language

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