

END SEMESTER EXAMINATION

MAY-2012

EP-214 MICROPROCESSOR AND INTERFACING

Time: 3:00 Hours

Max. Marks : 70

Note : Question No. **ONE** is compulsory.
 Attempt **SEVEN** questions in all.
 Assume suitable missing data, if any.

- Ques.1. (a) Explain the Special Mask Mode in 8259. (2)
 (b) What is the duration of the bus cycle for a 5 MHz 8086 that is running at full speed and with no wait states? (2)
 (c) What should be the status of the pins A0 and A1 of the 8255 such that the ports and the control register are selected? (2)
 (d) Explain the operation of the following instruction in PIC-16F877:
 MOVWF, 10H (2)
 (e) Explain the term handshaking signals as it applies to computer I/O devices. (2)
- Ques. 2. (a) Briefly describe the various bits of status register in PIC-16F877. (4)
 (b) The contents of CS and IP in 8086 are 1234H and 5678H. Obtain the 20-bit effective/physical address to be used in the address bus for accessing the specified location. (2)
 (c) What would be the effect of the following instruction sequence on the AX register:
 PUSH AX
 ADD AX, 4
 POP BX
 MOV CX, AX
 PUSH BX
 POP AX (2)
 (d) Which flags are tested in the following instruction:
 JAE ABOVE (2)
- Ques. 3. (a) Write an instruction sequence to design an interfacing circuit to read data from an A/D converter using 8255.
 (i.) Set up Port B to read data.
 (ii.) Set up bit PC₁ to start conversion and bit PC₆ to read the ready status of the converter (6)
 (b) Explain the CCP Mode of interface in PIC-16F877. (4)

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