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# DIGITAL DESIGN

WITH AN INTRODUCTION TO THE VERILOG HDL

M. Morris Mano | Michael D. Ciletti



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Digital Design and Computer Architecture, RISC-V Edition Sarah Harris, David Harris, 2021-07-12 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books this RISC V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor By the end of this book readers will be able to build their own RISC V microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing a RISC V processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun's RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems Digital Design and Computer Architecture, ARM Edition Sarah Harris, David Harris, 2015-04-09 Digital Design and Computer Architecture ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor By the end of this book readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing an ARM processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with

practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Features side by side examples of the two most prominent Hardware Description Languages HDLs SystemVerilog and VHDL which illustrate and compare the ways each can be used in the design of digital systems Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises DIGITAL DESIGN NATARAJAN, R. ANANDA, 2015-01-17 Primarily intended for undergraduate engineering students of Electronics and Communication Electronics and Electronics and Instrumentation Computer Science and Information Technology this book will also be useful for the students of BCA B Sc Electronics and CS M Sc Electronics and CS and MCA Digital Design is a student friendly textbook for learning digital electronic fundamentals and digital circuit design It is suitable for both traditional design of digital circuits and HDL based digital design This well organised text gives a comprehensive view of Boolean logic logic gates and combinational circuits synchronous and asynchronous circuits memory devices semiconductor devices and PLDs and HDL VHDL and Verilog programming Numerous solved examples are given right after conceptual discussion to provide better comprehension of the subject matter VHDL programs along with simulation results are given for better understanding of VHDL programming Key features Well labelled illustrations provide practical understanding of the concepts GATE level MCQs with answers along with detailed explanation wherever required at the end of each chapter help students to prepare for competitive examinations Short questions with answers and appropriate number of review questions at the end of each chapter are useful for the students to prepare for university exams and competitive exams Separate chapters on VHDL and Verilog programming along with simulated results are included to enhance the programming skills of HDL Challenges and Solutions in the Digital Economy and **Finance** Anna Rumyantseva, Vladimir Plotnikov, Alexey Minin, Hod Anyigba, 2022-11-04 This volume presents the proceedings of the 4th International Scientific and Practical Conference on Digital Economy and Finances DEFIN22 at the Saint Petersburg University of Management Technologies and Economics UMTE which took place in March 2022 It includes the newest research on the impact of new digital technologies on the growth and capitalization of companies and the labor market The volume discusses the problems of situational modeling of economic processes and the creation of digital twins of enterprises The contributions analyse how big data and artificial intelligence technologies are shaping the financial markets

Digital Design and Computer Architecture David Harris, Sarah Harris, 2010-07-26 Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material Digital Design, 1986 Digital Design and Manufacturing Technology Guo Zhong Chai, Cong Da Lu, Dong Hui Wen, 2010-03-09 Selected peer reviewed papers from the conference on Digital Design and Manufacturing 26 28 April 2010 Modern Digital Design Richard S. Sandige, 1990 Covers the principles of designing Hangzhou City Zhejiang Province digital electronic circuits and presents realistic applications using integrated circuit devices. The book also discusses ways to utilize programmable logic device software and hardware Engineering Education ,1979 **Digital Design Fundamentals** Kenneth James Breeding, 1992 **Introduction to Logic Design** Svetlana N. Yanushkevich, Vlad P. Shmerko, 2008-01-25 With an abundance of insightful examples problems and computer experiments Introduction to Logic Design provides a balanced easy to read treatment of the fundamental theory of logic functions and applications to the design of digital devices and systems Requiring no prior knowledge of electrical circuits or electronics it supplies the essential material to understand the basic operation and design of digital systems Satisfying the Requirements of an Introductory Course The text presents a clear picture of basic concepts effective problem solving techniques modern technologies and applications It focuses on the relationships between and the manipulation of various data structures By examining the similarities and differences between assorted theoretical approaches for the representation manipulation and optimization of Boolean functions the authors provide a unified overview of the relationships among digital system design computer organization micro and nanoelectronics and numerical methods The book also describes classical minimization techniques along with advanced and emerging problems of logic design Meeting the Needs of Developing Technology Reflecting the integrated nature of modern engineering this text shows how theoretical ideas physical devices and design methodologies come together to form a successful design approach It provides the basis to explore even more sophisticated aspects in the

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Formal Methods in Architecture Sara Eloy, David Leite Viana, Franklim Morais, Jorge Vieira Vaz, 2021-01-04 This edited book gathers research studies presented at the 5th International Symposium on Formal Methods in Architecture 5FMA Lisbon 2020 Studies focus on the use of methodologies especially those that have witnessed recent developments that stem from the mathematical and computer sciences and are developed in a collaborative way with architecture and related fields This book constitutes a contribution to the debate and to the introduction of new methodologies and tools in the mentioned fields that derive from the application of formal methods in the creation of new explicit languages for problem solving in architecture and urbanism It adds valuable insight into the development of new practices solving identified societal problems and promoting the digital transformation of institutions in the mentioned fields The primary audience of this book will be from the fields of architecture urban planning civil engineering AEC landscape design computer sciences and mathematics both academicians and professionals Practical Digital Design Using ICs Joseph D. Greenfield, 1994 New third edition offers a start to finish approach to digital circuit design beginning with simple circuits and advancing to highly complex circuits Coverage runs from simple circuits easily constructed in the laboratory through complex circuits such as those used in memory systems computers and computer interfacing including many examples of analysis and design A solid introductory guide for electrical electronics technicians and hobbvists The Michigan Technic ,1950 **Science and Technology** .1964 **International Science and Technology** ,1964

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