

# COURSE CODE : ECE131

## COURSE TITLE : BASIC ELECTRICAL AND ELECTRONICS ENGINEERING

Time Allowed: 03:00 hrs

Read the following instructions carefully before attempting the question paper.

Max. Marks: 70

1. Match the Paper Code shaded on the OMR Sheet with the Paper code mentioned on the question paper and ensure that both are the same.
2. This question paper is divided into two parts A and B.
3. Part A contains 20 questions of 1 mark each. 0.25 marks will be deducted for each wrong answer.
4. Part B contains 5 questions of 10 marks each. In each question attempt either question (a) or (b), in case both (a) and (b) questions are attempted for any question then only the first attempted question will be evaluated.
5. Attempt all the questions in serial order.
6. Do not write or mark anything on the question paper except your registration no. on the designated space.
7. After completion of first 45 minutes, the OMR sheet will be taken by the invigilator.
8. Submit the question paper and the rough sheet(s) along with the answer sheet to the invigilator before leaving the examination hall.

### PART A

Q1.

1)

Silicon is a/an \_\_\_\_\_ semiconductor

- (a) extrinsic (b) intrinsic (c) impure (d) doped

2) Germanium's resistance is cut in half for every \_\_\_\_\_ rise in temperature

- (a) 1 degree celsius (b) 5 degree Celsius (c) 10 degree celsius (d) 8 degree celsius

3) Depletion region in a p-n junction diode consist of

- (a) mobile ions (b) both (a) and (c) (c) immobile ions (d) none of the above

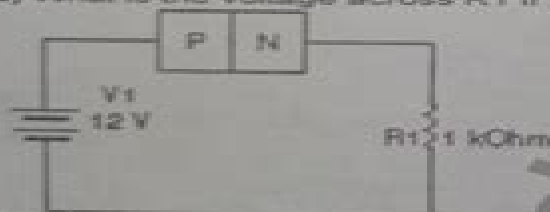
4) Gallium arsenide phosphide (GaAsP) made LEDs emit

- (a) visible green light (b) visible blue light  
(c) visible red light (d) colour depends upon amount of Ga AsP

5) Semiconductor materials generally have \_\_\_\_\_ of resistance

- (a) no temperature effect (b) positive temperature coefficient  
(c) negative temperature coefficient (d) zero temperature coefficient

6) What is the voltage across R1 if the P-N junction is made of silicon?



- a) 12V b) 11.7V c) 11.3V d) 0V
- 7) In an octal number system, every number is represented by a set of:
- a) two bits b) three bits c) five bits d) four bits
- 8) For a NAND gate, if the both the input are 1, the output will be:
- a) 1 b) 0 c) either 0 or 1 d) none of the above
- 9) For XOR Gate, the output is high when :
- a) both inputs are 1 b) Both inputs are zero  
c) any one input is high. d) Both (a) & (c)
- 10) Convert the binary number 001101 into octal number:
- a) 15 b) 16 c) 34 d) 51
- 11) In SR Flip Flop, when both inputs are high then flip flop will be in
- a) Set State b) Reset State c) No change state d) Toggle state

# Electrical Engineering Question Paper For Be Ece

**PT Brinkman**



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       29th Annual *Frontiers in Education Conference* Puerto Rico) *Frontiers in Education Conference* (29th : 1999 : San Juan,1999     *Electrical Computer Engineering* University of Wisconsin--Madison. Department of Electrical and Computer Engineering,2005     **Electrical Review** ,1886     **United Nations Documents Index** United Nations Staff,2003 The United Nations Documents Index covers documents and publications issued by the United Nations offices worldwide The publication indexes a wide variety of documentation such as major reports and studies resolutions and decisions draft resolutions and meeting records including documents of restricted distribution The information in this publication is arranged in the following nine sections documents and publications official records sales publications United Nations maps included in UN documents United Nations sheet maps United Nations document series symbols author index title index and subject index     **Conference Proceedings** ,2004     *Engineering News and American Contract Journal* ,1903     *Signal Processing, Sensor Fusion, and Target Recognition* ,1994

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