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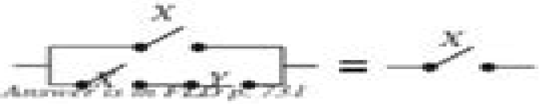
(a) In both cases, if X = 0, the transmission is 0, and if X = 1, the transmission is 1.



2.3 Answer is in FLD p. 731

2.4 (a) $F = [(A \cdot 1) + (A \cdot 1)] + E + BCD = A + E + BCD$

2.5 (a) $(A + B)(C + B)(D' + B)(ACD' + E)$
$= (AC + B)(D' + B)(ACD' + E)$ By Dist. Law
$= (ACD' + B)(ACD' + E)$ By Dist. Law
$= ACD' + BE$ By Dist. Law

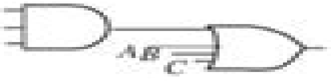
2.6 (a) $AB + C'D' = (AB + C')(AB + D')$
$= (A + C')(B + C')(A + D')(B + D')$

2.6 (c) $A'BC + EF + DEF' = A'BC + E(F + DF')$
$= A'BC + E(F + D) = (A'BC + E)(A'BC + F + D)$
$= (A' + E)(B + E)(C + E)(A' + F + D)$

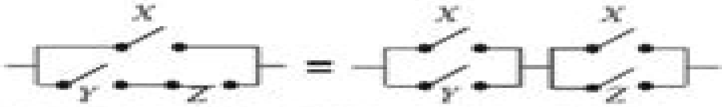
2.6 (e) **2.8 (a)**

2.8 (c)

2.7 (a)



2.2 (b) In both cases, if X = 0, the transmission is YZ, and if X = 1, the transmission is 1.



2.4 (b) $Y = (AB' + (AB + B))B + A = (AB' + B)B + A$
$= (A + B)B + A = AB + B + A = A + B$

2.5 (b) $(A' + B + C')(A' + C' + D)(B' + D')$
$= (A' + C' + BD)(B' + D')$
{By Distributive Law with $X = A' + C'$}
$= A'B' + B'C' + B'BD + A'D' + C'D' + BDD'$
$= A'B' + A'D' + C'B' + C'D'$

2.6 (b) $WX + WY'X + ZYX = X(W + WY' + ZY)$
$= X(W + ZY)$ {By Absorption}
$= X(W + Z)(W + Y)$

2.6 (d) $XYZ + W'Z + XQ'Z = Z(XY + W' + XQ')$
$= Z[W' + X(Y + Q')]$
$= Z(W' + X)(W' + Y + Q')$ By Distributive Law

(B $(AC' + C') + A'C$

+ $= D'(A + C') + A'C$ By Elimination Theorem
F $= (D' + A'C)(A + C' + A'C)$
+ $= (D' + A')(D' + C)(A + C' + A')$
D) By Distributive Law and Elimination Theorem
(C $= (A' + D')(C + D')$
+ $(\underline{A + B + C} + D)(\underline{A + B + C} + E)(\underline{A + B + C} + F)$
F $= \underline{A + B + C} + DEF$
+ Apply second Distributive Law twice
D)

ACD' D
$+ C'D'$ E
$+ A'C$ F
$= D'$

Contemporary Logic Design Solution Manual

Christian Drosten

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Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu,2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology **FPGA Prototyping by VHDL Examples** Pong P. Chu,2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller **Embedded SoPC Design with Nios II Processor and VHDL Examples** Pong P. Chu,2011-08-29 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes

FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www altera com university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration **Contemporary Logic Design** Randy H. Katz,1994 This text demonstrates state of the art technologies for the design of modern logic circuits including CAD tools rapid prototyping and programmable logic devices It provides practice in traditional techniques of logic design and includes examples of implementations from many CAD tools **Closing the Gap Between ASIC & Custom** David Chinnery,Kurt William Keutzer,2002 This book carefully details design tools and techniques for high performance ASIC design Using these techniques the performance of ASIC designs can be improved by two to three times Important topics include Improving performance through microarchitecture Timing driven floorplanning Controlling and exploiting clock skew High performance latch based design in an ASIC methodology Automatically identifying and synthesizing complex logic gates Automated cell sizing to increase performance and reduce power Controlling process variation These techniques are illustrated by designs running two to three times the speed of typical ASICs in the same process generation Digital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAs Steven T. Karris,2007 This book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits It is self contained **Subject Guide to Books in Print** ,1996 **Contemporary Logic Design** Randy H. Katz,Gaetano Borriello,2006 **British Books in Print** ,1985 **Contemporary Logic Design** Randy H. Katz,1994 The Journal of Symbolic Logic Alonzo Church,Cooper Harold Langford,1967 Includes lists of members **Catalog of Copyright Entries. Third Series** Library of Congress. Copyright Office,1968 Engineering Education ,1980 **Forthcoming Books** Rose Arny,1999 *Advanced Digital Logic Design* Sunggu Lee,2006 This textbook is intended to serve as a practical guide for the design of complex digital logic circuits such as digital control circuits network interface circuits pipelined arithmetic units and RISC microprocessors It is an advanced digital logic design textbook that emphasizes the use of synthesizable Verilog code and provides numerous fully worked out practical design examples including a Universal Serial Bus interface a pipelined multiply accumulate unit and a pipelined microprocessor for the ARM THUMB architecture **Books and Pamphlets, Including Serials and Contributions to Periodicals** Library of Congress. Copyright Office,1973 *The Logic Design of Transistor Digital Computers* Gerald A. Maley,John Earle,1963 Book Review Digest ,1922 *The American School* ,1919 **Catalogue of Title-entries of Books and Other Articles Entered in the Office of the Librarian of Congress, at Washington, Under the Copyright Law ... Wherein the Copyright Has Been Completed by**

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