

POKHARA UNIVERSITY

Level: Bachelor
 Programme: BCA
 Course: Digital Logic System (New)

Semester: Fall

Year : 2023
 Full Marks: 100
 Pass Marks: 45
 Time : 3hrs.

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

- | | | | |
|----|-----|--|-----|
| 1. | a). | Perform the following conversions: | 8 |
| | | i. $(E1B.AC5)_{16} = (?)_8$ | |
| | | ii. $(237)_8 = (?)_2$ | |
| | | iii. $(100110101)_{gray} = (?)_2$ | |
| | | iv. $(1110)_2 = (?)_{10}$ | |
| | b). | Show NAND and NOR gates as universal gates. | 7 |
| 2. | a) | What is the dual of a function? Explain involution law. | 7 |
| | b) | Simplify the following Boolean function with don't care condition.
$F(A, B, C, D) = \sum m(0, 1, 3, 4, 5, 6, 7, 11)$
$d(A, B, C, D) = \sum m(8, 10, 12, 15)$ | 8 |
| | | Draw the logic circuit using NOR gates only. | |
| 3. | a) | Implement a circuit full adder. | 8 |
| | b) | Convert Excess-3 to BCD code and use K-map for circuit implementation. | 7 |
| 4. | a) | What is JK flipflop? Explain its operation with the help of logic circuit and truth table. | 8 |
| | b) | Explain state diagram and state table. Explain it with example. | 7 |
| 5. | a) | Explain PISO shift register. | 8 |
| | b) | Design a 3-bit synchronous down ripple counter. | 7 |
| 6. | a) | What is status register? Explain about RAM. | 8 |
| | b) | Draw the circuit and explain 4-bit ALU. | 7 |
| 7. | | Write short notes on: (Any two) | 2×5 |
| | a) | Gray Code | |
| | b) | Postulates of boolean algebra | |
| | c) | Maxterm vs Minterm | |

Digital Logic Question Paper

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A red circular graphic element with a gradient, partially overlapping the right side of the grey bar.

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Table of Contents Digital Logic Question Paper

1. Understanding the eBook Digital Logic Question Paper
 - The Rise of Digital Reading Digital Logic Question Paper
 - Advantages of eBooks Over Traditional Books
2. Identifying Digital Logic Question Paper
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Digital Logic Question Paper
 - User-Friendly Interface

4. Exploring eBook Recommendations from Digital Logic Question Paper
 - Personalized Recommendations
 - Digital Logic Question Paper User Reviews and Ratings
 - Digital Logic Question Paper and Bestseller Lists
5. Accessing Digital Logic Question Paper Free and Paid eBooks
 - Digital Logic Question Paper Public Domain eBooks
 - Digital Logic Question Paper eBook Subscription Services
 - Digital Logic Question Paper Budget-Friendly Options
6. Navigating Digital Logic Question Paper eBook Formats
 - ePub, PDF, MOBI, and More
 - Digital Logic Question Paper Compatibility with Devices
 - Digital Logic Question Paper Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Digital Logic Question Paper
 - Highlighting and Note-Taking Digital Logic Question Paper
 - Interactive Elements Digital Logic Question Paper
8. Staying Engaged with Digital Logic Question Paper
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Digital Logic Question Paper
9. Balancing eBooks and Physical Books Digital Logic Question Paper
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Digital Logic Question Paper
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Digital Logic Question Paper
 - Setting Reading Goals Digital Logic Question Paper
 - Carving Out Dedicated Reading Time

12. Sourcing Reliable Information of Digital Logic Question Paper
 - Fact-Checking eBook Content of Digital Logic Question Paper
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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