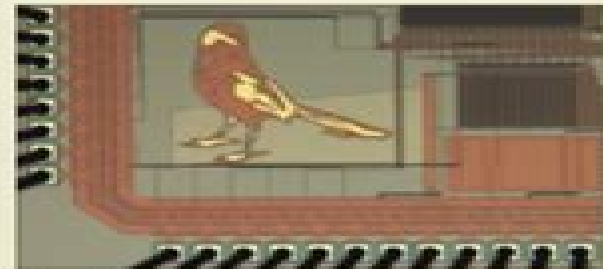
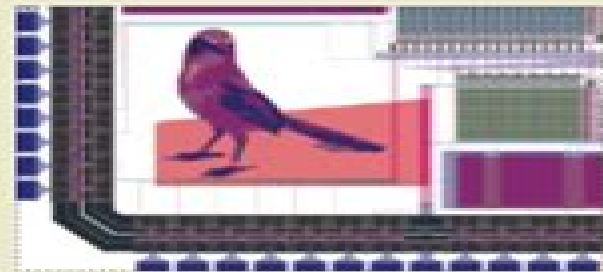


# Digital VLSI Chip Design with Cadence and Synopsys CAD Tools



Erik Brunvand

# Digital Design Lab Manual For Cadence

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