

Digital Design

with RTL Design, VHDL, and Verilog

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Digital Design with RTL Design, VHDL, and Verilog Frank Vahid, 2010-03-09 An eagerly anticipated up to date guide to essential digital design fundamentals Offering a modern updated approach to digital design this much needed book reviews basic design fundamentals before diving into specific details of design optimization You begin with an examination of the low levels of design noting a clear distinction between design and gate level minimization The author then progresses to the key uses of digital design today and how it is used to build high performance alternatives to software Offers a fresh up to date approach to digital design whereas most literature available is sorely outdated Progresses through low levels of design making a clear distinction between design and gate level minimization Addresses the various uses of digital design today Enables you to gain a clearer understanding of applying digital design to your life With this book by your side you will gain a better understanding of how to apply the material in the book to real world scenarios

Digital Design Frank Vahid, 2006 Digital Design provides a modern approach to learning the increasingly important topic of digital systems design The text's focus on register transfer level design and present day applications not only leads to a better appreciation of computers and of today's ubiquitous digital devices but also provides for a better understanding of careers involving digital design and embedded system design The book's key features include An emphasis on register transfer level RTL design the level at which most digital design is practiced today giving readers a modern perspective of the field's applicability Yet coverage stays bottom up and concrete starting from basic transistors and gates and moving step by step up to more complex components Extensive use of basic examples to teach and illustrate new concepts and of application examples such as pacemakers ultrasound machines automobiles and cell phones to demonstrate the immediate relevance of the concepts Separation of basic design from optimization allowing development of a solid understanding of basic design before considering the more advanced topic of optimization Flexible organization enabling early or late coverage of optimization methods or of HDLs and enabling choice of VHDL Verilog or SystemC HDLs Career insights and advice from designers with varying levels of experience A clear bottom up description of field programmable gate arrays FPGAs About the Author Frank Vahid is a Professor of Computer Science has worked consulted for Hewlett Packard AMCC NEC Motorola and medical equipment makers holds 3 U S patents has received several teaching awards helped setup UCR's Computer Engineering program has authored two previous textbooks and has published over 120 papers on digital design topics automation architecture and low power

Digital Design Frank Vahid, 2006-07-28 While most popular digital design books present a perspective rooted in the 1970s and 1980s Digital System Design takes the subject into the 21st century It quickly moves through the low levels of design making a clear distinction between design and gate level minimization The book also emphasizes how one of the key uses of digital design today is to build high performance alternatives to software in addition to glue logic And it swiftly progresses to register transfer level RTL design since that is the level at which most digital design

in practice today is performed *APCCAS ...* ,1994 **Euro-DAC '95, European Design Automation Conference with Euro-VHDL** ,1995 **EURO-DAC ...** ,1995 *Mergent OTC Industrial Manual* ,2003 Choice ,2006 Comprehensive Dissertation Index ,1989 *Books in Print* ,1993-09 V 1 Authors A D v 2 Authors E K v 3 Authors L R v 4 S Z v 5 Titles A D v 6 Titles E K v 7 Titles L Q v 8 Titles R Z v 9 Out of print out of stock indefinitely v 10 Publishers **Books In Print 2004-2005** Ed Bowker Staff,Staff Bowker, Ed,2004 *Digital Manual* Guan Lee,Daniel Widrig,2020

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