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Digital Integrated Circuits

Analysis & Design

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break the analog design bottleneck CMOS Digital Integrated Circuits Sung-Mo Kang, Yusuf Leblebici, Chulwoo Kim, 2014-05 Offers comprehensive coverage of digital CMOS circuit design as well as addressing technology issues highlighted by the widespread use of nanometer scale CMOS technologies Subject Guide to Books in Print, 1993

CMOS Digital Integrated Circuits Analysis & Design Sung-Mo (Steve) Kang, Yusuf Leblebici, Chul Woo Kim, 2014-01-24 CMOS Digital Integrated Circuits Analysis and Design continues the well established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design as well as addressing state of the art technology issues highlighted by the widespread use of nanometer scale CMOS technologies In this latest edition virtually all chapters have been rewritten the transistor model equations and device parameters have been revised to reflect the sigificant changes that must be taken into account for new technology generations and the material has been reinforced with up to date examples The broad ranging coverage of this textbook starts with the fundamentals of CMOS process technology and continues with MOS transistor models basic CMOS gates interconnect effects dynamic circuits memory circuits arithmetic building blocks clock and I O circuits low power design techniques design for manufacturability and design for testability

Comparators in Nanometer CMOS Technology Bernhard Goll, Horst Zimmermann, 2014-09-15 This book covers the complete spectrum of the fundamentals of clocked regenerative comparators their state of the art advanced CMOS technologies innovative comparators inclusive circuit aspects their characterization and properties Starting from the basics of comparators and the transistor characteristics in nanometer CMOS seven high performance comparators developed by the authors in 120nm and 65nm CMOS are described extensively Methods and measurement circuits for the characterization of advanced comparators are introduced A synthesis of the largely differing aspects of demands on modern comparators and the properties of devices being available in nanometer CMOS which are posed by the so called nanometer hell of physics is accomplished The book summarizes the state of the art in integrated comparators Advanced measurement circuits for characterization will be introduced as well as the method of characterization by bit error analysis usually being used for characterization of optical receivers The book is compact and the graphical quality of the illustrations is outstanding This book is written for engineers and researchers in industry as well as scientists and Ph D students at universities It is also recommendable to graduate students specializing on nanoelectronics and microelectronics or circuit design Solutions Manual Digital Integrated Circuits CRC Press, 2009-07-10 Any textbook more than five years old simply won t do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization Each chapter includes numerous worked examples case studies and SPICE computer simulations The book s website offers supplementary material and more worked problems Qualifying instructors will have access to a new instructor

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The British National Bibliography Arthur James Wells, 1994 **CMOS Digital Integrated Circuits** Charles F. Hawkins, Jaume Segura, Payman Zarkesh-ha, 2013 Modern CMOS Circuits Manual R. M. Marston, 1996 This Circuits Manual examines operating principles and practical applications of modern medium speed and fast CMOS digital ICs 470 carefully selected circuits diagrams graphs and tables are supported by the informative how to text and by detailed descriptions of more than 120 modern CMOS ICs and their practical applications Although ideal for practical design engineers and technicians this book will doubtless also be of great interest to hobbyists and students of electronics Using clear and comprehensive language each chapter begins with an explanation of the basic principles of the subject followed by the presentation of circuits and useful data The first chapter describes and explains digital IC basics CMOS and TTL principles the various CMOS sub families and CMOS basic usage rules Chapter 2 gives a practical introduction to CMOS basics via the 4007UB IC which can be used in both digital and linear applications Chapter 3 deals with modern logic circuitry and Chapter 4 with CMOS bilateral switches and data selectors The next six chapters progress through waveform generator circuitry clocked flip flop and counter circuits ICs special counter dividers data latches registers comparators and code converters Chapter 11 focuses on specialised types of IC such as multiplexers and decoders while the final chapter presents a miscellaneous collection of useful CMOC circuits **CMOS Digital Integrated Circuits** Charles Hawkins, Jaume Segura, Payman Zarkesh-Ha, 2013 CMOS Digital Integrated Circuits A First Course teaches the fundamentals of modern CMOS technology by focusing on central themes and avoiding overwhelming details Extensive examples self exercises and end of chapter problems assist in teaching the current practices of industry and subjects taught by graduate courses in microelectronics Computer engineering curriculums can remove the analog electronics prerequisite altogether when adopting this book This book is also unique in that it presents timing the most difficult of the computer designer s tasks and an issue that is avoided by all other textbooks The remaining chapters describe memory metal thermal and capacitive properties FPGAs layout and then concludes with a chapter on how circuits are made in a chip factory Supplementary materials for professors are available upon request via email to books theiet org **CMOS Logic Circuit Design** John P. Uyemura, 2007-05-08 This is an up to date treatment of the analysis and design of CMOS integrated digital logic circuits The self contained book covers all of the important digital circuit design styles found in modern CMOS chips emphasizing solving design problems using the various logic styles available in CMOS CMOS Digital Integrated Circuits, 2018-05 The past 25 years have seen enormous growth in the capability and ubiquity of digital integrated circuits In the mid 1980s the industry had moved to CMOS technology for high performance digital design due to the power problems with both NMOS and bipolar technology Complementary metal oxide semiconductor CMOS digital integrated circuits are the enabling technology for the

modern information age Because of their intrinsic features in low power consumption large noise margins and ease of design CMOS integrated circuits have been widely used to develop random access memory RAM chips microprocessor chips digital signal processor DSP chips and application specific integrated circuit ASIC chips The popular use of CMOS circuits will grow with the increasing demands for low power low noise integrated electronic systems in the development of portable computers personal digital assistants PDAs portable phones and multimedia agents This book covers the complete treatment of CMOS circuits basic design concepts with detailed examples Trend in digital integrated circuits is discussed with basic topologies used for designing circuits using CMOS transistors viz Static logic Dynamic logic and Domino logic are explained It typically addresses both the computer aided analysis issues and the design issues for most of the circuit examples Numerous research with results carried out in recent years on domino logic are also provided for illustration of basic concepts Through rigorous analysis of CMOS circuits in this volume students and research practitioners will be able to understand the fundamentals of CMOS VLSI design which is the driving force behind the development of advanced computer hardware

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