II B.Tech I Semester Regular Examinations, November 2007 ELECTRONIC CIRCUIT ANALYSIS

(Common to Electronics & Communication Engineering and Electronics & Telematics)

Time: 3 hours

Max Marks: 80

Answer any FIVE Questions All Questions carry equal marks

- (a) With the help of necessary equations, discuss the variation of A_I, A_V, R_I, and R_o with R_S and R_L in Common Emitter configuration.
 - (b) For a CE configuration, what is the maximum value of R_S for which R_σ differs by no more than 10 percent of its value for R_S = 0. The h-parameter values are h_{fe} = 50, h_{ic} =1.1 KΩ, h_{re} = 2.5 x10⁻⁴, h_{oc} = 25 μA/V. [10+6]
- A two-stage amplifier circuit (CE-CC configuration) is shown in figure 2. The h-parameter values are h_{fe} = 50, h_{be}=2 K Ω, h_{re} =6× 10⁻⁴, h_{oe} = 25µA/V. h_{fe} = -51, h_{be} =2 KΩ, h_{re} = 1, h_oc = 25µ A/V.
 Find the input and output impedances and individual, as well as overall voltage

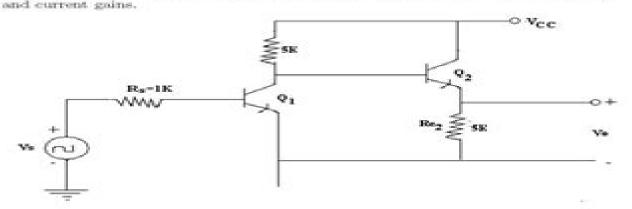


Figure 2:

- (a) Draw Hybrid π model for a transistor in the CE configuration and explain the significance of every component in this model.
 - (b) Given a germanium p-n-p transistor whose basewidth is 10⁻⁴ cm. At room temperature and for a dc emitter current of 2 mA, find

emitter diffusion capacitance,

f_T [Assume Diffusion constant as 47 cm²/sec].

[8 + 8]

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