

Allegro PCB Power Delivery Network Analysis

An integrated design and analysis environment for faster development of power-optimized PCBs

Cadence® Allegro® PCB Power Delivery Network Analysis lets you bring your PCB design from start to power-related issues throughout the design process and speed time-to-market. tightly integrated with Allegro PCB Designer enables you to rapidly modify the design and re-analyze through analysis iterations without time-consuming board fabrication iterations. Using this unified environment, you can create power guidelines early in the design process, make changes on-the-fly, and address the PCB challenge of consistent electrical performance under changing loads.

Allegro PCB PDN Analysis

Power delivery network (PDN) analysis is a critical component of contemporary PCB design. Higher frequency signals being processed by faster multi-processor chips demand far greater design discipline. It can be difficult to find the right balance of features (driven by the need to reduce PCB costs by decreasing copper layer and substrate board thickness) with the need designed for board shape to fit many dual and component footprints.

PCB designers and electrical engineers can use Cadence's fully integrated Allegro PCB Designer and Allegro PCB PDN analysis environment, which enables rapid design modification and automatic iterative analysis feeding back through board construction and error grossing iterations. This unified design-to-analysis environment lets you work early in the design process with various features, board area, thickness, with-drift, source and sink capacitance, to meet strict design guidelines and to optimize the PCB layout.

Real-time analysis enables accurate modeling of the design of impedance, power and ground planes, impedance

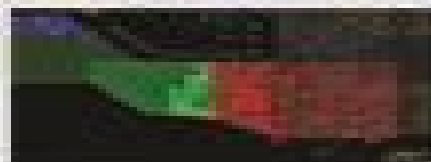


Figure 1 (Left): Voltage drop visualization of a PCB layout.

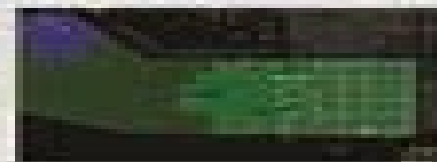


Figure 1 (Right): Design structure visualization of a PCB layout.

Figure 1. (Left) Allegro PCB PDN analysis environment that enables integrated design, analysis, and modification. (Right) Design structure visualization of a PCB layout.

to impedance on physical and grossing planes, ensuring that routing and other board layout can be optimized with safety. The visualization of the design structure shows design and test board complexity, including board thickness, area, and area-to-weight ratio, to ensure that the design is optimized for the design development cycle.

Benefits

- Accelerate your electrical design cycle time
- Keep your electrical design cycle time short and your PCB design cycle time short

- Analyze design complexity with various and high-level design structure analysis
- Analyze impedance for power, ground, and signal planes on the PCB
- Optimize board layout, including board area, thickness, and area-to-weight ratio
- Integrate seamlessly with Allegro PCB Designer and other Cadence design environments

Cadence Allegro Pdn Manual

SJ Ball



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