

Practice paper – 4

Topic – Basic BJT Amplifiers

1. Consider the following common-emitter amplifier circuit (Figure 1). Estimate the Q-point, small signal voltage gain, input and output resistances. Given that $\beta = 65$ and $V_A = 50$ V. Assume $V_{BE_{ON}} = 0.7$ V.

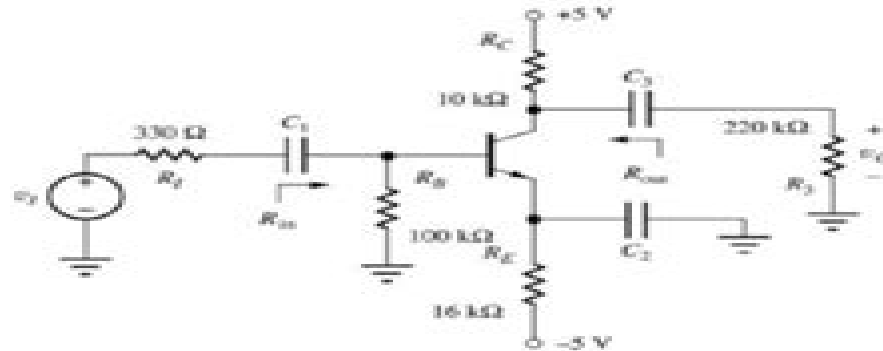


Figure 1

2. For the following amplifier circuit (shown in Fig. 2) the Si BJT has $\beta = 200$ and $V_A = 150$ V.

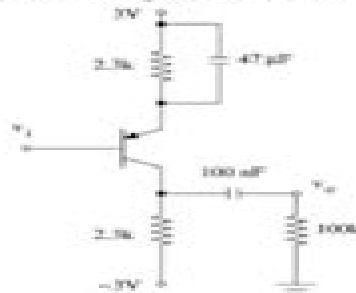


Figure 2

- Calculate I_C , I_B , I_E , V_{EC} . You can ignore the Early effect in DC bias calculations.
- Draw the AC equivalent circuit and determine the AC model parameters.
- Calculate the voltage gain.

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