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CMOS, Two Volume Set R. Jacob Baker, 2002-06-06

Low-power Low-noise CMOS Integrated Circuits for Bioelectronic Applications Jungsuk Kim, 2011 *Jahangirnagar University Journal of Science*, 2012

Analog Circuit Design for Embedded and High Performance Processors in Nanoscale Technologies Fadi Hikmat Gebara, 2005

Cmos Circuit Design Layout And Simulation Baker Li & Boyce, 2004

Chip Design for Submicron VLSI John Paul Uyemura, 2006 This book teaches the principles of physical design layout and simulation of CMOS integrated circuits It is written around a very powerful CAD program called Microwind that is available on the accompanying CD ROM Featuring a friendly interface Microwind is both educational and useful for designing CMOS chips

Symbolic Analysis Techniques Francisco Fernández, 1998 Electrical Engineering Symbolic Analysis Techniques Applications to Analog Design Automation Symbolic Analysis Techniques is a collection of original contributions from renowned experts in the field presenting the most recent and important applications of symbolic analysis to analog circuit design This timely self contained volume features an in depth tutorial introduction to the techniques and algorithms underlying modern symbolic analyzers and includes many references at the end of each chapter Applications are discussed in a variety of important fields Automatic generation of optimum circuit topologies Interactive circuit improvement and automated design space exploration Non fixed topology analog synthesis tools Semiconductor parameter extraction Analog testability and fault diagnosis And many more related areas Symbolic Analysis Techniques also features an extensive comparison of modern symbolic analyzer characteristics and limitations Brimming with practical instructions on tasks like formula simplification and post processing this book will be of use and interest to graduate students researchers and engineers involved in computer aided circuits analysis and analog design automation

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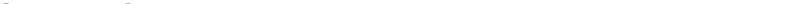
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