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Low-power Low-noise CMOS Integrated Circuits for Bioelectronic Applications Jungsuk Kim, 2011 *Jahangirnagar University Journal of Science*, 2012

Analog Circuit Design for Embedded and High Performance Processors in Nanoscale Technologies Fadi Hikmat Gebara, 2005

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Chip Design for Submicron VLSI John Paul Uyemura, 2006 This book teaches the principles of physical design layout and simulation of CMOS integrated circuits It is written around a very powerful CAD program called Microwind that is available on the accompanying CD ROM Featuring a friendly interface Microwind is both educational and useful for designing CMOS chips

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