

CMOS VLSI Design

A Circuits and Systems Perspective

Fourth Edition

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Building Embedded Systems Changyi Gu, 2016-05-26 Develop the software and hardware you never think about We're talking about the nitty gritty behind the buttons on your microwave inside your thermostat inside the keyboard used to type this description and even running the monitor on which you are reading it now Such stuff is termed embedded systems and this book shows how to design and develop embedded systems at a professional level Because yes many people quietly make a successful career doing just that Building embedded systems can be both fun and intimidating Putting together an embedded system requires skill sets from multiple engineering disciplines from software and hardware in particular Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project Programmers who know software will learn what they need to know about hardware Engineers with hardware knowledge likewise will learn about the software side Whatever your background is Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems He brings knowledge of numerous approaches to embedded systems design including the System on Programmable Chips SOPC approach that is currently growing to dominate the field His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field or even just to do some embedded programming as a side project What You Will Learn Program embedded systems at the hardware level Learn current industry practices in firmware development Develop practical knowledge of embedded hardware options Create tight integration between software and hardware Practice a workflow leading to successful outcomes Build from transistor level to the system level Make sound choices between performance and cost Who This Book Is For Embedded system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware Those who favor the System on a Programmable Chip SOPC approach will in particular benefit from this book Students in both Electrical Engineering and Computer Science can also benefit from this book and the real life industry practice it provides

Simulation and Optimization of Digital Circuits Vazgen Melikyan, 2018-04-12 This book describes new fuzzy logic based mathematical apparatus which enable readers to work with continuous variables while implementing whole circuit simulations with speed similar to gate level simulators and accuracy similar to circuit level simulators The author demonstrates newly developed principles of digital integrated circuit simulation and optimization that take into consideration various external and internal destabilizing factors influencing the operation of digital ICs The discussion includes factors including radiation ambient temperature electromagnetic fields and climatic conditions as well as non ideality of interconnects and power rails

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System Test and Testable Design Zainalabedin Navabi, 2010-12-10 This book is about digital system testing and testable design. The concepts of testing and testability are treated together with digital design practices and methodologies. The book uses Verilog models and testbenches for implementing and explaining fault simulation and test generation algorithms. Extensive use of Verilog and Verilog PLI for test applications is what distinguishes this book from other test and testability books. Verilog eliminates ambiguities in test algorithms and BIST and DFT hardware architectures and it clearly describes the architecture of the testability hardware and its test sessions. Describing many of the on-chip decompression algorithms in Verilog helps to evaluate these algorithms in terms of hardware overhead and timing and thus feasibility of using them for System on Chip designs. Extensive use of testbenches and testbench development techniques is another unique feature of this book. Using PLI in developing testbenches and virtual testers provides a powerful programming tool interfaced with hardware described in Verilog. This mixed hardware software environment facilitates description of complex test programs and test strategies.

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Digest of Technical Papers, 2001 Visual Communications and Image Processing '96, 1996 **Computer Aids for VLSI Design** Steven M. Rubin, 1987 This textbook originally published in 1987 broadly examines the software required to design electronic circuitry including integrated circuits. Topics include synthesis and analysis tools, graphics and user interface, memory representation and more. The book also describes a real system called Electric.

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